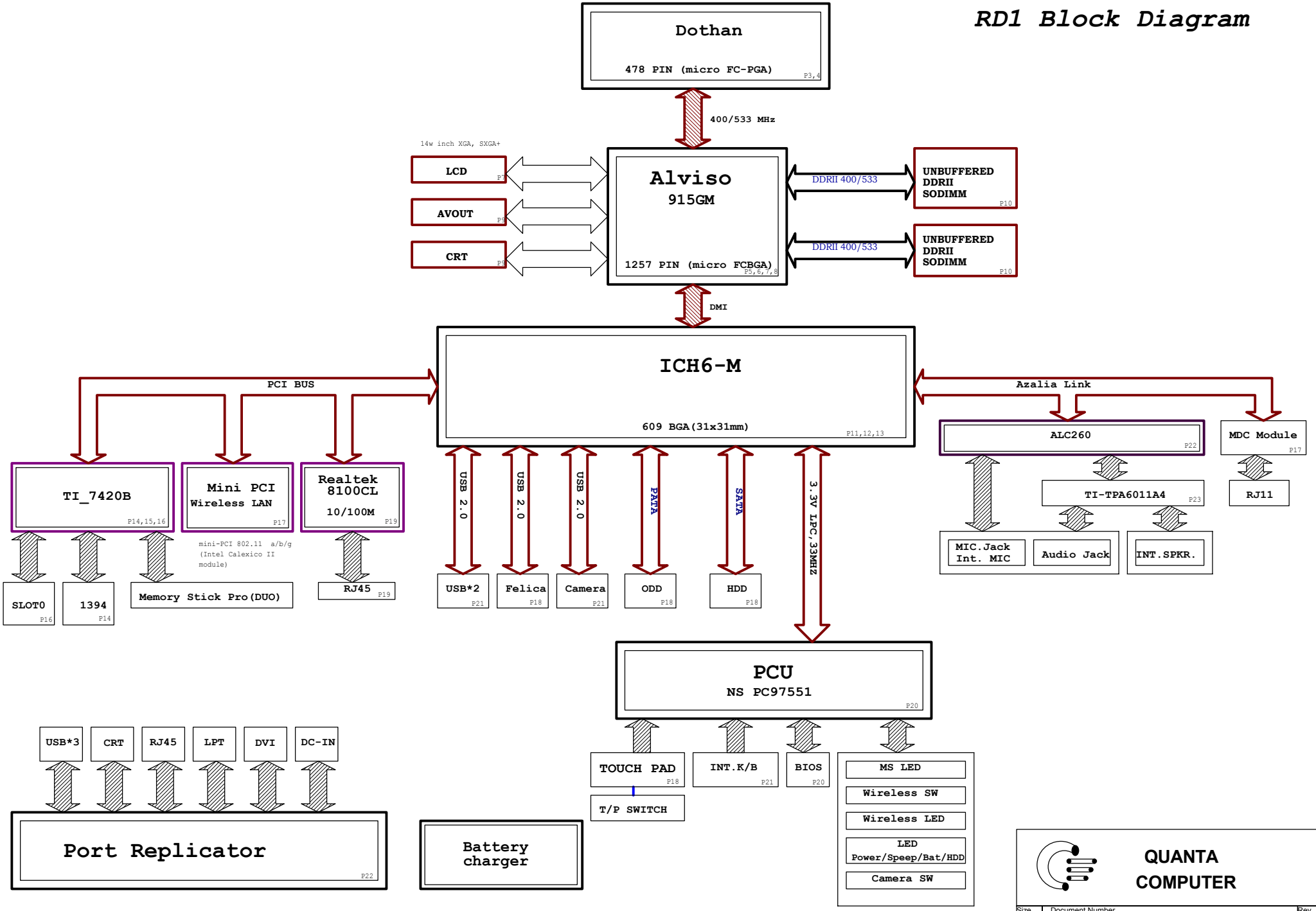
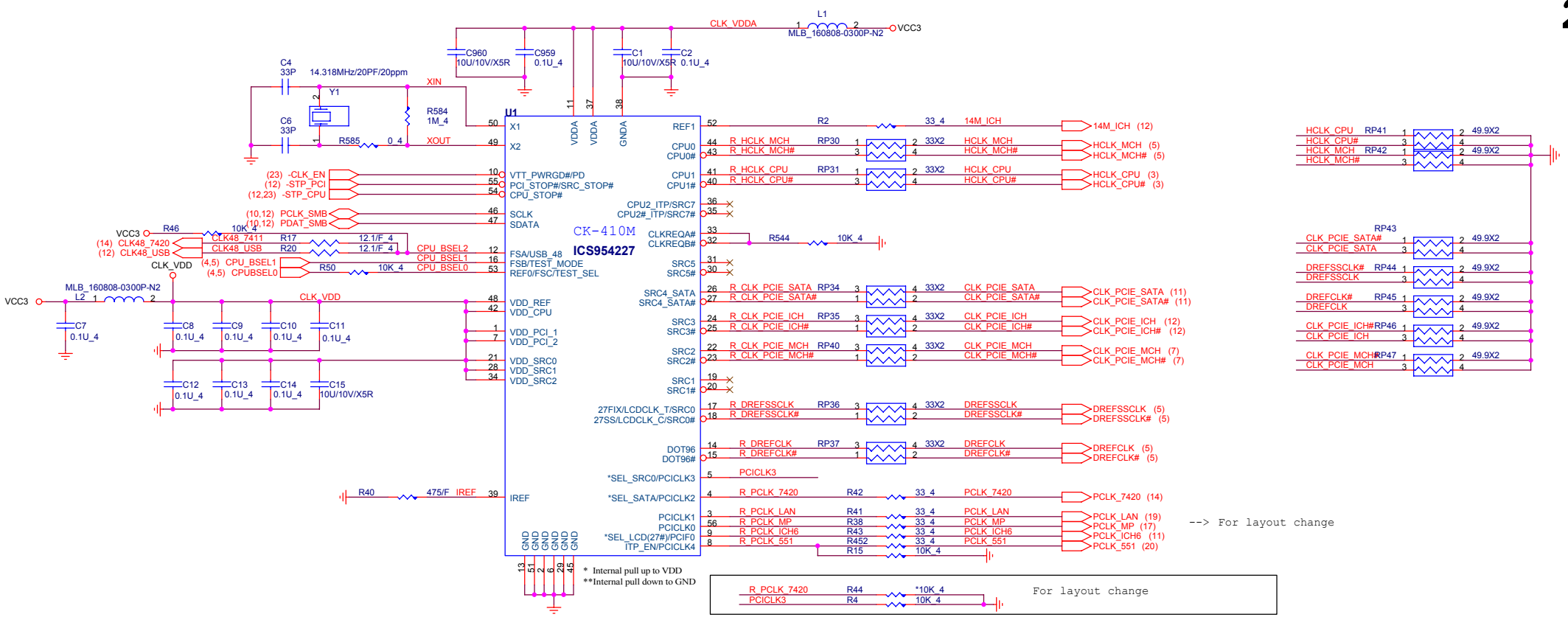
RD1 Block Diagram



QUANTA COMPUTER

Size	Document Number	Rev
	RD1 Main Board	1A
Date: Saturday, June 25, 2005	Sheet 1 of 32	



R_PCLK_7420 R44 *10K_4 — For layout change
PCICLK3 R4 10K_4

```
R44:(Pin 26,27)
  1: Clock from SARA PLL
  0: Clock from RC PLL
R4:(Pin 17,18)
  1: SRCCLK0 pair
  0: 17,18 pin is set by R6

R6:(Pin 17,18)
  1: LCDCLK pair
  0: 27MHZSS/27MHZSS# pair
```

96MHz SST Table :

S3	S2	S1	S0	Spread	Type
0	0	0	0	0.8	Down
0	0	0	1	1	
0	0	1	0	1.25	
0	0	1	1	1.5	
0	1	0	0	1.75	
0	1	0	1	2	
0	1	1	0	2.5	
0	1	1	1	3	
1	0	0	0	+/-0.3	Center
1	0	0	1	+/-0.4	
1	0	1	0	+/-0.5	
1	0	1	1	+/-0.6	
1	1	0	0	+/-0.8	
1	1	0	1	+/-1.0	
1	1	1	0	+/-1.25	
1	1	1	1	+/-1.5	

FSC BSEL0	FSB BSEL1	FSA BSEL2	CPU	SRC	PCI	REF	USB	DOT
	0	0	0	266.66	100	33.33	14.318	48	96
*	**0**	**0**	**1**	**133.33**	100	33.33	14.318	48	96
	0	1	0	200.00	100	33.33	14.318	48	96
	0	1	1	166.66	100	33.33	14.318	48	96
	1	0	0	333.33	100	33.33	14.318	48	96
*	**1**	**0**	**1**	**100.00**	100	33.33	14.318	48	96
	1	1	0	400.00	100	33.33	14.318	48	96
	1	1	1	RESERVE			14.318	48	96

ICS 954227 : Include SST

ICS 954227 Function:

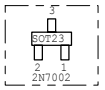
Pin 53 : REF1
Pin 17,18: 96MHz_SST

ITP_EN Function(Pin 8)

0 : SRC_7 Pair (Pin 35,36)

1 : CPU_2_ITP Pair (Pin 35,36)

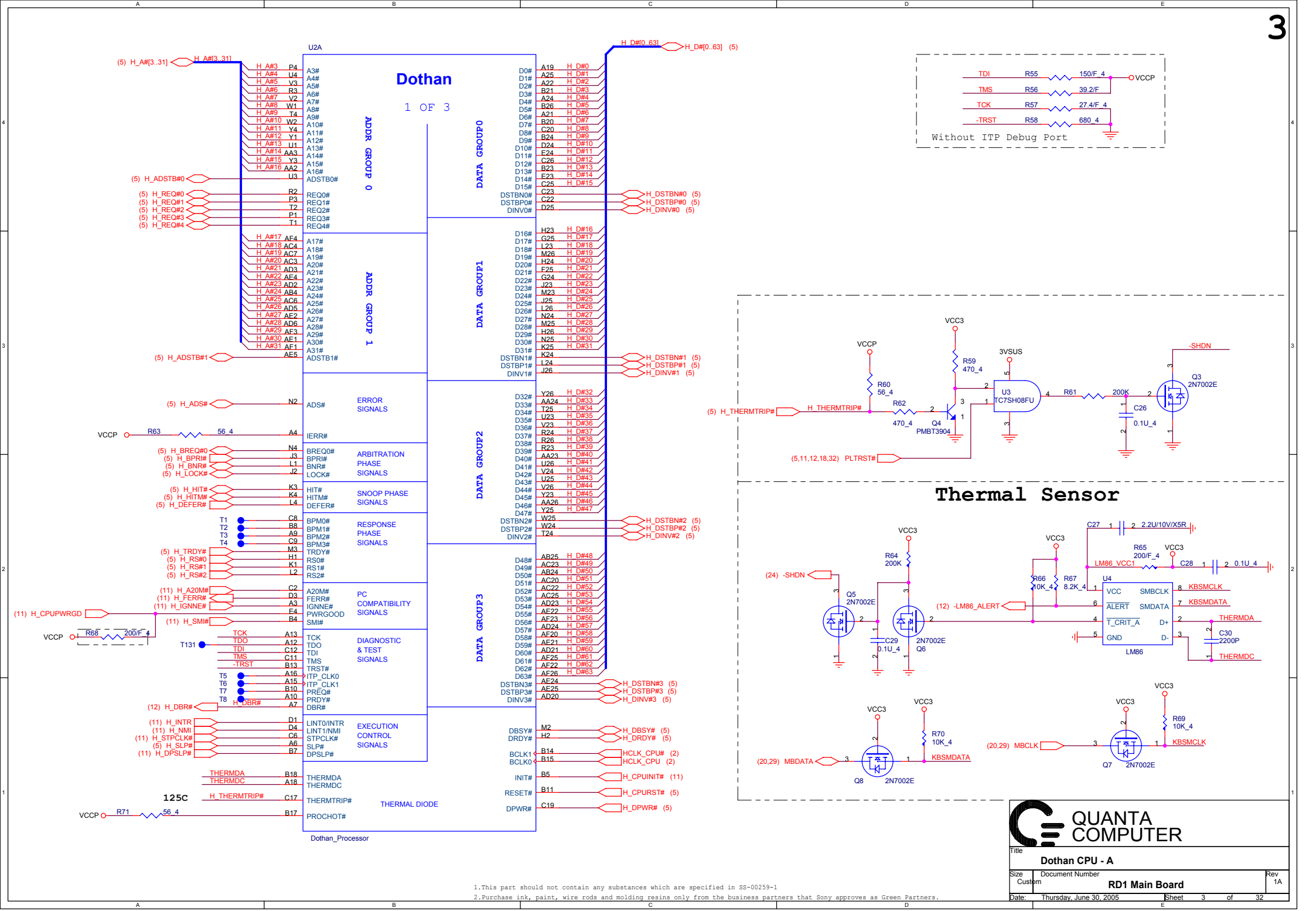
IREF (Pin 39) :
475_1% : Sets the IREF current to 2.32mA



1.This part should not contain any substances which are specified in SS-00259-1
2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

QUANTA COMPUTER

Title: CLOCK GENERATOR
Size: Custom | Document Number: RD1 Main Board | Rev: 1A
Date: Thursday, June 30, 2005 | Sheet 2 of 32



1.This part should not contain any substances which are specified in SS-00259-1

2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

QUANTA COMPUTER

Title	Dothan CPU - A	
Size: Custom	Document Number: RD1 Main Board	Rev: 1A
Date: Thursday, June 30, 2005	Sheet 3 of 32	

Place voltage divider within 0.5" of GTLREF pin

R72 27.4/F_4 COMP0 P25
R73 54.9/F_4 COMP1 P26
R74 27.4/F_4 COMP2 AB2
R75 54.9/F_4 COMP3 AB1

VCCP R76 1K/F_4 GTLREF 55ohm AD26
R77 2K/F
T92 E26 G1 AC1

R78 *1K_4 TEST1 C5
R79 *1K_4 TEST2 F23
C16
C14

(2,5) CPUBSEL0
(2,5) CPU_BSEL1

BSEL0:
H: 100MHz
L:133MHz

U2B

COMP0 COMP1 COMP2 COMP3
GTLREF0 RSVD5 DPRSTP# RSVD4
TEST1 TEST2 BSEL0/TEST3 BSEL1

Dothan
2 OF 3

POWER, GROUND, RESERVED SIGNALS

(6,20,26,27,28,30) MAINON

U5
CNOISE SD
DELAY ERROR
GND SENSE
VIN VOUT
*SI9181-15

VCC3
C3 *10U/6.3V/X5R
C33 *0.1U_4
C34 0.1U_4
CPU_VCCA

VCC1.5 L48 MLB_160808-0300P-N2 (1.5V,300mA)
CPU_VCCA
C31 10U/6.3V/X5R
C32 0.01U_4

AC26 N1 B1 F26
VCCA3 VCCA2 VCCA1 VCCA0

VCC_CORE (27A)

D6 D8 D18 D20 D22 E5 E7 E9 E17 E19 E21 F6 F8 F18 F20 F22 G5 G21 H6 H22 J5 J21 K22 U5 V6 V22 W5 W21 Y6 Y22 AA5 AA7 AA9 AA11 AA13 AA15 AA17 AA19 AA21 AB6 AB8 AB10 AB12 AB14 AB16 AB18 AB20 AB22 AC9 AC11 AC13 AC15 AC17 AC19 AD8 AD10 AD12 AD14 AD16 AD18 AE9 AE11 AE13 AE15 AE17 AE19 AF8 AF10 AF12 AF14 AF16 AF18

VCC00 VCC01 VCC02 VCC03 VCC04 VCC05 VCC06 VCC07 VCC08 VCC09 VCC10 VCC11 VCC12 VCC13 VCC14 VCC15 VCC16 VCC17 VCC18 VCC19 VCC20 VCC21 VCC22 VCC23 VCC24 VCC25 VCC26 VCC27 VCC28 VCC29 VCC30 VCC31 VCC32 VCC33 VCC34 VCC35 VCC36 VCC37 VCC38 VCC39 VCC40 VCC41 VCC42 VCC43 VCC44 VCC45 VCC46 VCC47 VCC48 VCC49 VCC50 VCC51 VCC52 VCC53 VCC54 VCC55 VCC56 VCC57 VCC58 VCC59 VCC60 VCC61 VCC62 VCC63 VCC64 VCC65 VCC66 VCC67 VCC68 VCC69 VCC70 VCC71

VSS A2 A5 A8 A11 A14 A17 A20 A23 A26 B3 B6 B9 B12 B16 B19 B22 B25 C1 C4 C7 C10 C13 C15 C18 C21 C24 D2 D5 D7 D9 D11 D13 D15 D17 D19 D21 D23 D26 E3 E6 E8 E10 E12 E14 E16 E18 E20 E22 E25 F1 F4 F5 F7 F9 F11 F13 F15 F17 F19 F21 F24 G2 G6 G22 G23 G26 H3 H5 H21 H25 J1 J4 J6 J22 J24 K2 K5 K21 K23 K26 L3 L6 L22 L25 M1 M4 M5 M21 M24 N3 N6 N22 N23 N26 P2 P5 P21 P24 R1 R4

Dothan_Processor

1.356 ~ 1.260V : Max.Frequency mode
1.052V : lowest frequency mode
0.748V : Deeper Sleep

U2C

VCCP (1.05V,3A)
D10 D12 D14 D16 E11 E13 E15 F10 F12 F14 F16 K6 L5 L21 M6 M22 N5 N21 P6 P22 R5 R21 T6 T22 U21 P23 W4
VCCP0 VCCP1 VCCP2 VCCP3 VCCP4 VCCP5 VCCP6 VCCP7 VCCP8 VCCP9 VCCP10 VCCP11 VCCP12 VCCP13 VCCP14 VCCP15 VCCP16 VCCP17 VCCP18 VCCP19 VCCP20 VCCP21 VCCP22 VCCP23 VCCP24 VCCQ0 VCCQ1

Dothan
3 OF 3

POWER, GROUND AND NC

(23) CPU_VID0 (23) CPU_VID1 (23) CPU_VID2 (23) CPU_VID3 (23) CPU_VID4 (23) CPU_VID5
E2 F2 F3 G3 G4 H4
VID0 VID1 VID2 VID3 VID4 VID5

1.05V OUTPUT VID

T93 AE7 VCCSENSE
T94 AF6 VSSSENSE

T9 B2 NC0
T10 AE7 NC1
T11 C3 NC3

T12 E1 PSI

R6 R22 R25 T3 T5 T21 T23 T26 U2 U6 U22 U24 V1 V4 V5 V21 V25 W3 W6 W22
VSS

Dothan_Processor

VCC_CORE

C37 1 2 470U/2.5V

C35 0.1U_4
C36 0.1U_4
C38 0.1U_4
C39 0.1U_4

C40 10U/6.3V/X5R
C41 10U/6.3V/X5R
C47 10U/6.3V/X5R
C49 10U/6.3V/X5R
C51 10U/6.3V/X5R

C42 0.01U_4
C48 0.01U_4
C50 0.01U_4
C52 0.01U_4

C58 10U/6.3V/X5R
C60 10U/6.3V/X5R
C62 10U/6.3V/X5R
C69 10U/6.3V/X5R
C71 10U/6.3V/X5R

C59 10U/6.3V/X5R
C61 10U/6.3V/X5R
C63 10U/6.3V/X5R
C70 10U/6.3V/X5R
C72 10U/6.3V/X5R

C73 10U/6.3V/X5R
C78 10U/6.3V/X5R
C80 10U/6.3V/X5R
C82 10U/6.3V/X5R
C84 10U/6.3V/X5R

C74 10U/6.3V/X5R
C79 10U/6.3V/X5R
C81 10U/6.3V/X5R
C83 10U/6.3V/X5R
C85 10U/6.3V/X5R

C86 10U/6.3V/X5R
C88 10U/6.3V/X5R
C90 10U/6.3V/X5R
C92 10U/6.3V/X5R
C94 10U/6.3V/X5R

C87 10U/6.3V/X5R
C89 10U/6.3V/X5R
C91 10U/6.3V/X5R
C93 10U/6.3V/X5R
C95 10U/6.3V/X5R

10U/6.3V/X5R(CC0805) 5 mOhm*35

VCCP

C43 0.1U_4
C44 0.1U_4
C45 0.1U_4
C46 0.1U_4

C53 0.1U_4
C54 0.1U_4
C55 0.1U_4
C56 0.1U_4
C57 0.1U_4

C64 0.1U_4
C65 *0.1U_4
C66 *0.1U_4
C67 *0.1U_4
C68 *0.1U_4

C75 *0.1U_4
C76 *0.1U_4
C77 *0.1U_4

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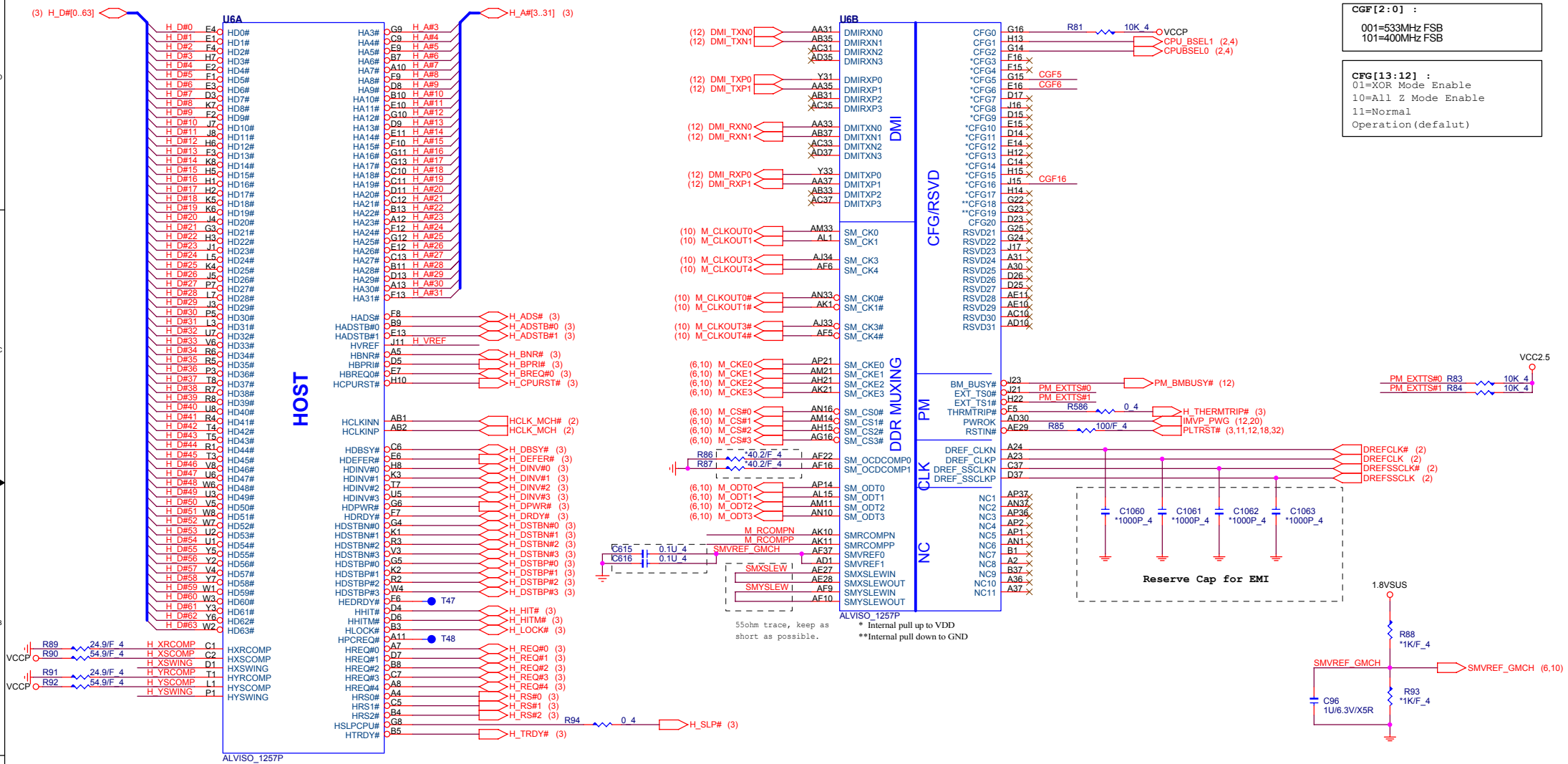
QUANTA COMPUTER

Title	Dothan CPU-B	
Size	Document Number RD1 Main Board	Rev 1A
Date: Thursday, June 30, 2005	Sheet 4 of 32	



CGF[2:0] :
001=533MHz FSB
101=400MHz FSB

CFG[13:12] :
01=XOR Mode Enable
10=All Z Mode Enable
11=Normal
Operation(defalut)

	Low	High
CGF5	★ DMIx2	DMIx4
CGF6	★ DDR II	DDR
CGF7	CPU type: DT/Transportable	★ CPU type: Mobile CPU
CGF9	PCIE Graphics lan : Reverse Lane	★ PCIE Graphics lan : Normal operation
CGF16	Dynamic ODT Disabled	★ Dynamic ODT Enabled
CGF18	★ Alviso GMCH core: 1.05V	Alviso GMCH core: 1.5V
CGF19	★ VPP Voltage : 1.05V	VPP Voltage : 1.2V

Note:
CFG[17:3] have internal pullup resistors.
CFG[19:18] have internal pulldown resistors.

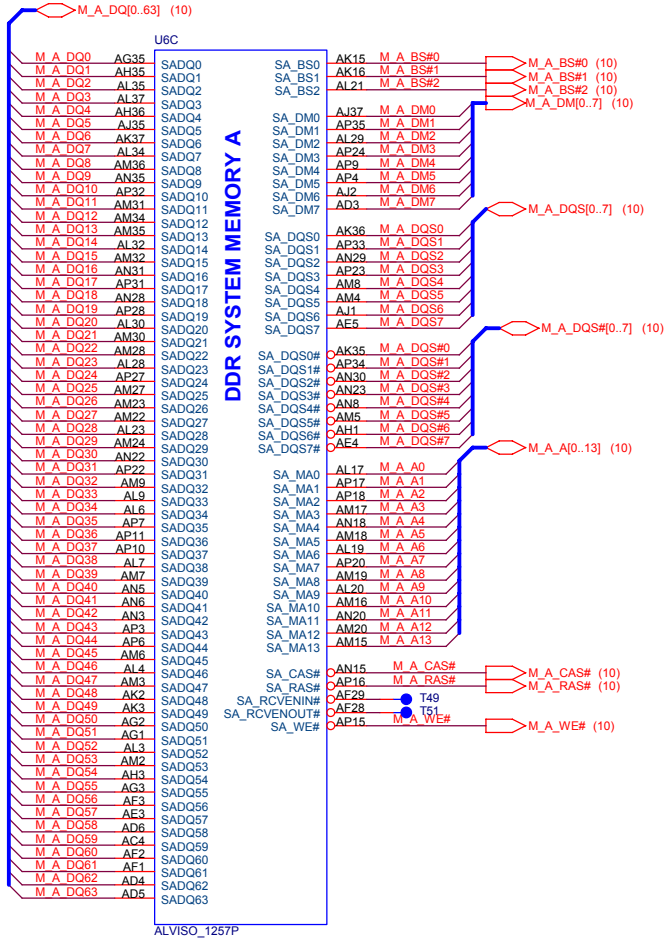
	H_SLP#
For Doathan A-X step	*0
★For Doathan B-X step & later	0

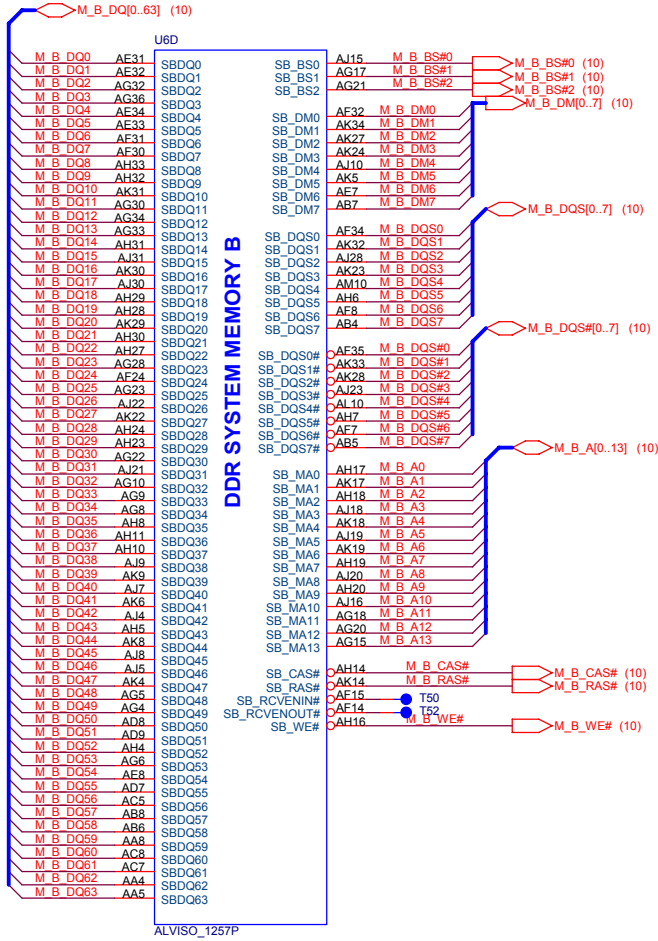
SDVOCRTL_DATA :
★ 0 : No SDVO Device Present(default)
1 : SDVO Device Presend

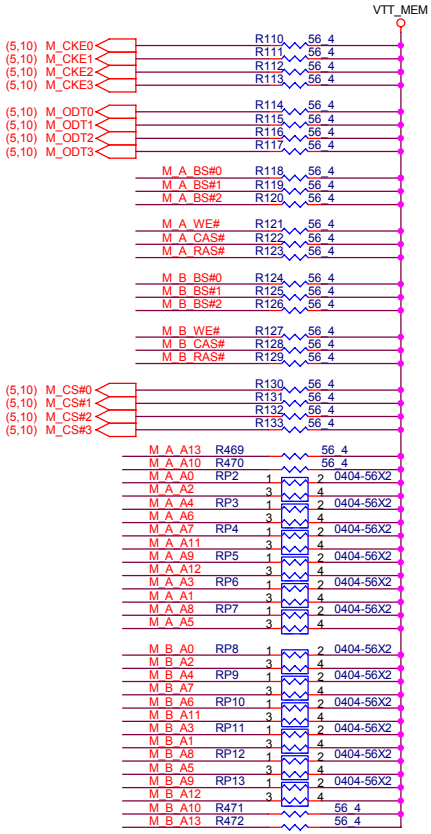
1.This part should not contain any substances which are specified in SS-00259-1
2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

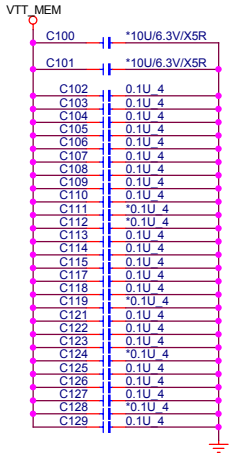
QUANTA COMPUTER

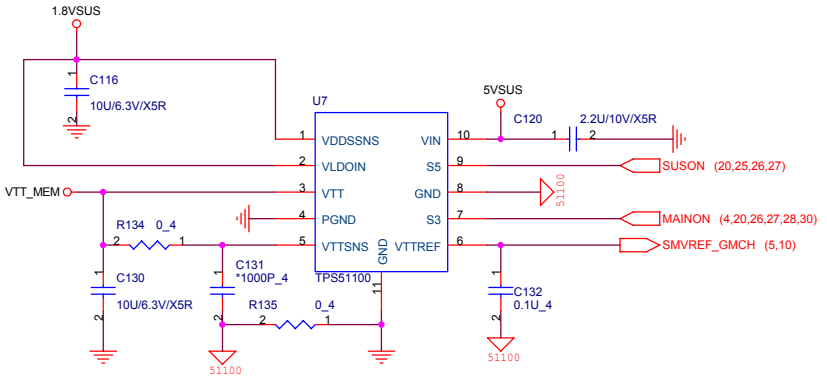
Title	Alviso	
Size	Document Number	Rev
	RD1 Main Board	1A
Date: Thursday, June 30, 2005	Sheet 5 of 32	











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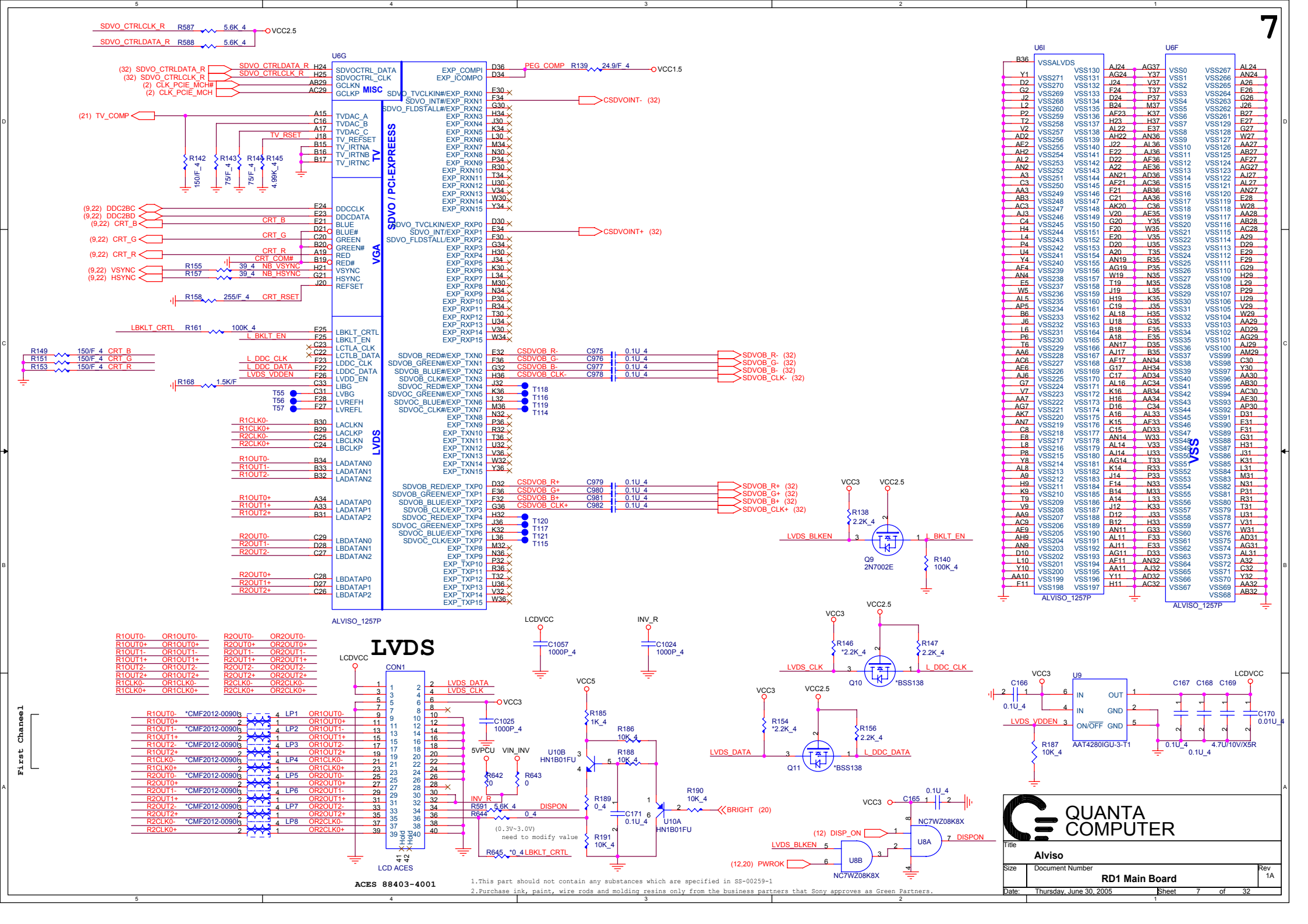
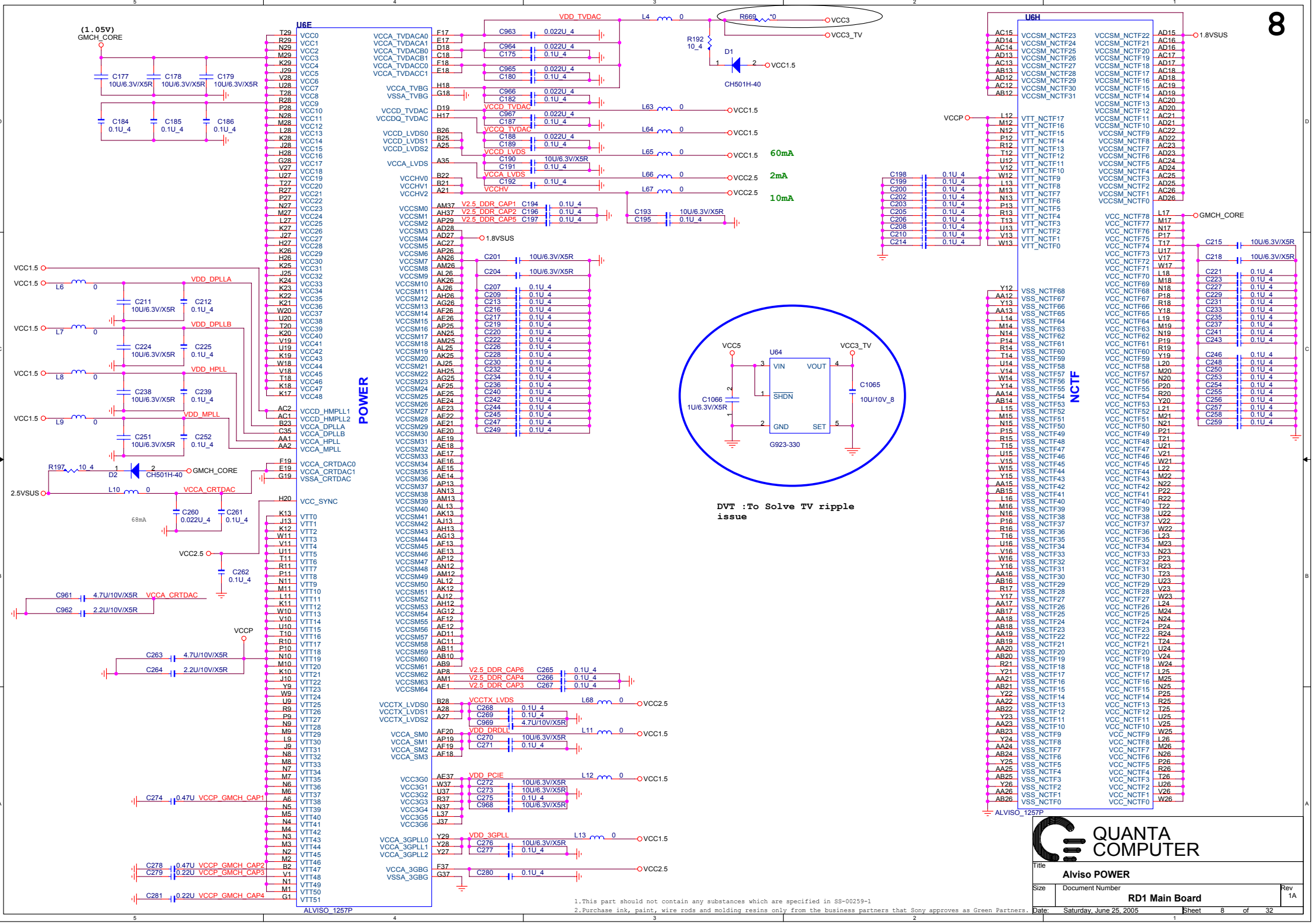
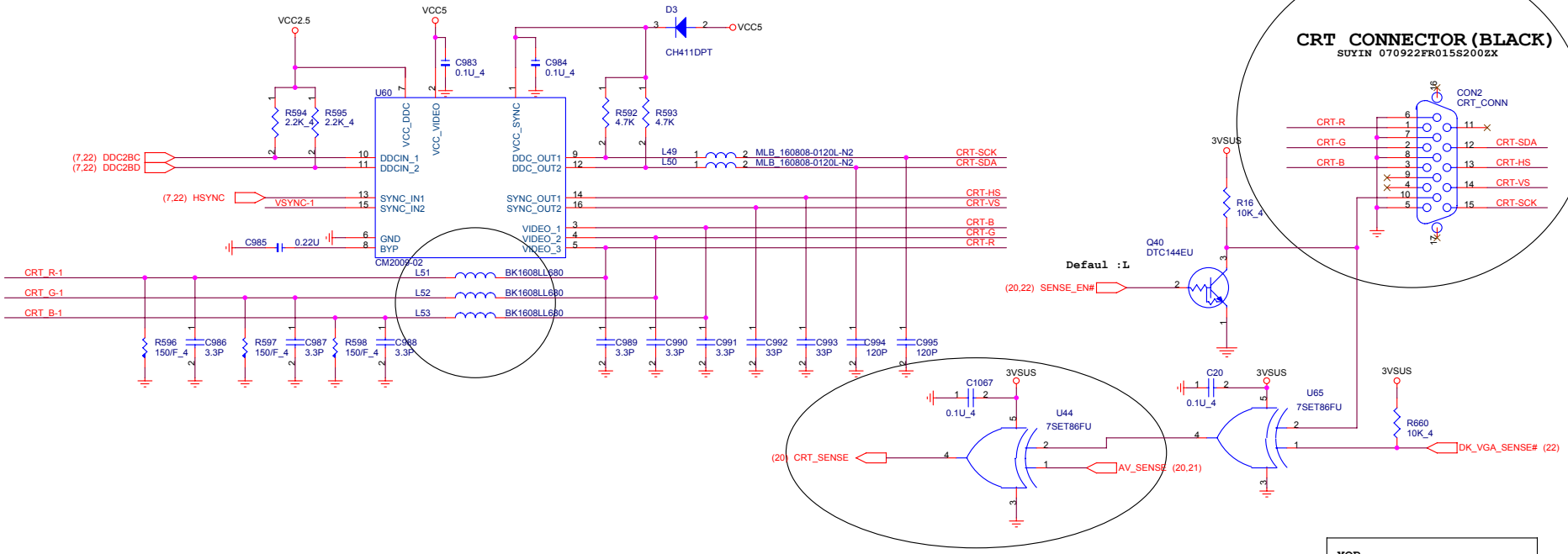
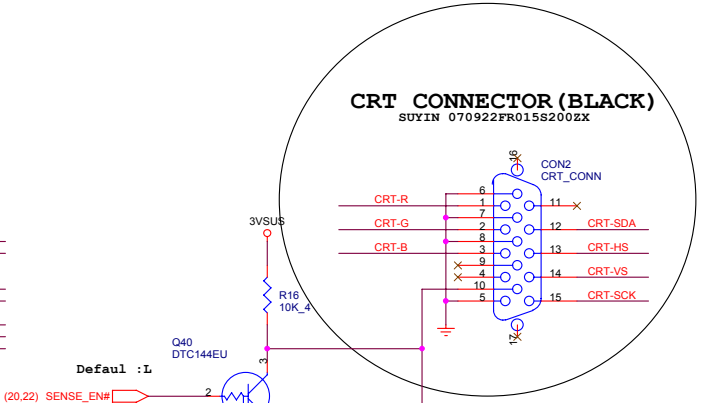
QUANTA COMPUTER

Title	Alviso-DDRII	
Size	Document Number: RD1 Main Board	Rev: 1A
Date: Thursday, June 30, 2005	Sheet 6 of 32	

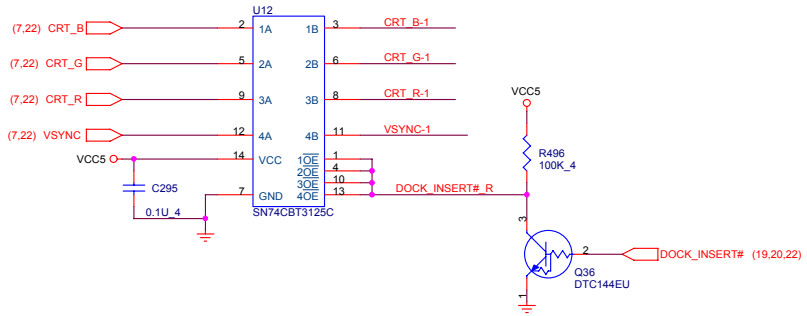

7
SDVO_CTRLCLK_R R587 5.6K_4 VCC2.5
SDVO_CTRLDATA_R R588 5.6K_4
U6G
(32) SDVO_CTRLDATA_R
(32) SDVO_CTRLCLK_R
(2) CLK_PCIE_MCH#
(2) CLK_PCIE_MCH
SDVOCTRL_DATA
SDVOCTRL_CLK
GCLKN
GCLKP
MISC
EXP_COMPI
EXP_ICOMPO
PEG_COMP R139 24.9/F_4 VCC1.5
SDVO_TVCLKIN#/EXP_RXN0
SDVO_INT#/EXP_RXN1
SDVO_FLDSTALL#/EXP_RXN2
CSDVOINT- (32)
(21) TV_COMP
TV_RSET
TVDAC_A
TVDAC_B
TVDAC_C
TV_REFSET
TV_IRTNA
TV_IRTNB
TV_IRTNC
TV
R142 150/F_4
R143 75/F_4
R144 75/F_4
R145 4.99K_4
SDVO / PCI-EXPRESS
SDVO_TVCLKIN/EXP_RXP0
SDVO_INT/EXP_RXP1
SDVO_FLDSTALL/EXP_RXP2
CSDVOINT+ (32)
(9,22) DDC2BC
(9,22) DDC2BD
(9,22) CRT_B
(9,22) CRT_G
(9,22) CRT_R
(9,22) VSYNC
(9,22) HSYNC
DDCCLK
DDCDATA
BLUE
BLUE#
GREEN
GREEN#
RED
RED#
VSYNC
HSYNC
REFSET
CRT_COM#
R155 39_4 NB_VSYNC
R157 39_4 NB_HSYNC
R158 255/F_4 CRT_RSET
VGA
LBKLT_CRTL R161 100K_4
L_BKLT_EN
LBKLT_CRTL
LBKLT_EN
LCTLA_CLK
LCTLB_DATA
LDDC_CLK
LDDC_DATA
LVDD_EN
LIBG
LVBG
LVREFH
LVREFL
R149 150/F_4 CRT_B
R151 150/F_4 CRT_G
R153 150/F_4 CRT_R
L_DDC_CLK
L_DDC_DATA
LVDS_VDDEN
R168 1.5K/F
T55
T56
T57
LVDS
SDVOB_RED#/EXP_TXN0
SDVOB_GREEN#/EXP_TXN1
SDVOB_BLUE#/EXP_TXN2
SDVOB_CLK#/EXP_TXN3
SDVOC_RED#/EXP_TXN4
SDVOC_GREEN#/EXP_TXN5
SDVOC_BLUE#/EXP_TXN6
SDVOC_CLK#/EXP_TXN7
CSDVOB_R- C975 0.1U_4 SDVOB_R- (32)
CSDVOB_G- C976 0.1U_4 SDVOB_G- (32)
CSDVOB_B- C977 0.1U_4 SDVOB_B- (32)
CSDVOB_CLK- C978 0.1U_4 SDVOB_CLK- (32)
T118
T116
T119
T114
R1CLK0-
R1CLK0+
R2CLK0-
R2CLK0+
R1OUT0-
R1OUT1-
R1OUT2-
R2OUT0-
R2OUT1-
R2OUT2-
LACLKN
LACLKP
LBCLKN
LBCLKP
LADATAN0
LADATAN1
LADATAN2
LADATAP0
LADATAP1
LADATAP2
LBDATAN0
LBDATAN1
LBDATAN2
LBDATAP0
LBDATAP1
LBDATAP2
SDVOB_RED/EXP_TXP0
SDVOB_GREEN/EXP_TXP1
SDVOB_BLUE/EXP_TXP2
SDVOB_CLK/EXP_TXP3
SDVOC_RED/EXP_TXP4
SDVOC_GREEN/EXP_TXP5
SDVOC_BLUE/EXP_TXP6
SDVOC_CLK/EXP_TXP7
CSDVOB_R+ C979 0.1U_4 SDVOB_R+ (32)
CSDVOB_G+ C980 0.1U_4 SDVOB_G+ (32)
CSDVOB_B+ C981 0.1U_4 SDVOB_B+ (32)
CSDVOB_CLK+ C982 0.1U_4 SDVOB_CLK+ (32)
T120
T117
T121
T115
ALVISO_1257P
U6I
U6F
VSSALVDS
VSS
ALVISO_1257P
ALVISO_1257P
VCC3
VCC2.5
R138 2.2K_4
LVDS_BLKEN
BKLT_EN
Q9 2N7002E
R140 100K_4
LCDVCC
INV_R
C1057 1000P_4
C1024 1000P_4
R146 *2.2K_4
R147 2.2K_4
LVDS_CLK
L_DDC_CLK
Q10 *BSS138
LVDS
First Channel
R1OUT0- OR1OUT0-
R1OUT0+ OR1OUT0+
R1OUT1- OR1OUT1-
R1OUT1+ OR1OUT1+
R1OUT2- OR1OUT2-
R1OUT2+ OR1OUT2+
R1CLK0- OR1CLK0-
R1CLK0+ OR1CLK0+
R2OUT0- OR2OUT0-
R2OUT0+ OR2OUT0+
R2OUT1- OR2OUT1-
R2OUT1+ OR2OUT1+
R2OUT2- OR2OUT2-
R2OUT2+ OR2OUT2+
R2CLK0- OR2CLK0-
R2CLK0+ OR2CLK0+
*CMF2012-00901
LP1
LP2
LP3
LP4
LP5
LP6
LP7
LP8
LCDVCC
CON1
LVDS_DATA
LVDS_CLK
VCC3
C1025 1000P_4
5VPCU
VIN_INV
R642 0
R643 0
INV_R
R591 5.6K_4
R644 0_4
DISPON
(0.3V~3.0V)
need to modify value
R645 *0_4 LBKLT_CRTL
LCD ACES
ACES 88403-4001
VCC5
R185 1K_4
R186 10K_4
R188 10K_4
U10B HN1B01FU
R189 0_4
C171 0.1U_4
R191 10K_4
R190 10K_4
U10A HN1B01FU
BRIGHT (20)
VCC3
VCC2.5
R154 *2.2K_4
R156 2.2K_4
LVDS_DATA
L_DDC_DATA
Q11 *BSS138
VCC3
C165 0.1U_4
NC7WZ08K8X
U8A
U8B
(12) DISP_ON
LVDS_BLKEN
(12,20) PWROK
DISPON
C166 0.1U_4
U9
IN
OUT
GND
ON/OFF
LVDS_VDDEN
R187 10K_4
AAT4280IGU-3-T1
C167 0.1U_4
C168 0.1U_4
C169 4.7U/10V/X5R
C170 0.01U_4
LCDVCC
1.This part should not contain any substances which are specified in SS-00259-1
2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.
QUANTA COMPUTER
Title
Alviso
Size
Document Number
RD1 Main Board
Rev 1A
Date: Thursday, June 30, 2005
Sheet 7 of 32


U6E
POWER
ALVISO_1257P
U6H
NCTF
ALVISO_1257P
(1.05V)
GMCH_CORE
C177 10U/6.3V/X5R
C178 10U/6.3V/X5R
C179 10U/6.3V/X5R
C184 0.1U_4
C185 0.1U_4
C186 0.1U_4
VDD_TVDAC
L4 0
R669 *0
VCC3
VCC3_TV
R192 10_4
D1
VCC1.5
CH501H-40
VCCD_TVDAC
VCCQ_TVDAC
VCCD_LVDS
VCCA_LVDS
VCCHV
L63 0
L64 0
L65 0
L66 0
L67 0
60mA
2mA
10mA
C963 0.022U_4
C964 0.022U_4
C175 0.1U_4
C965 0.022U_4
C180 0.1U_4
C966 0.022U_4
C182 0.1U_4
C967 0.022U_4
C187 0.1U_4
C188 0.022U_4
C189 0.1U_4
C190 10U/6.3V/X5R
C191 0.1U_4
C192 0.1U_4
C193 10U/6.3V/X5R
C195 0.1U_4
V2.5_DDR_CAP1 C194 0.1U_4
V2.5_DDR_CAP2 C196 0.1U_4
V2.5_DDR_CAP5 C197 0.1U_4
1.8VSUS
C201 10U/6.3V/X5R
C204 10U/6.3V/X5R
C207 0.1U_4
C209 0.1U_4
C213 0.1U_4
C216 0.1U_4
C217 0.1U_4
C219 0.1U_4
C220 0.1U_4
C222 0.1U_4
C226 0.1U_4
C228 0.1U_4
C230 0.1U_4
C232 0.1U_4
C234 0.1U_4
C236 0.1U_4
C240 0.1U_4
C242 0.1U_4
C244 0.1U_4
C245 0.1U_4
C247 0.1U_4
C249 0.1U_4
VCC1.5
L6 0
L7 0
L8 0
L9 0
VDD_DPLLA
VDD_DPLLB
VDD_HPLL
VDD_MPLL
C211 10U/6.3V/X5R
C212 0.1U_4
C224 10U/6.3V/X5R
C225 0.1U_4
C238 10U/6.3V/X5R
C239 0.1U_4
C251 10U/6.3V/X5R
C252 0.1U_4
R197 10_4
D2
CH501H-40
GMCH_CORE
2.5VSUS
L10 0
VCCA_CRTDAC
C260 0.022U_4
C261 0.1U_4
68mA
VCC2.5
C262 0.1U_4
C961 4.7U/10V/X5R
C962 2.2U/10V/X5R
VCCP
C263 4.7U/10V/X5R
C264 2.2U/10V/X5R
C274 0.47U_VCCP_GMCH_CAP1
C278 0.47U_VCCP_GMCH_CAP2
C279 0.22U_VCCP_GMCH_CAP3
C281 0.22U_VCCP_GMCH_CAP4
VCC5
VCC3_TV
U64
VIN
VOUT
SHDN
GND
SET
G923-330
C1066 1U/6.3V/X5R
C1065 10U/10V_8
DVT :To Solve TV ripple issue
VCCP
L12
C198 0.1U_4
C199 0.1U_4
C200 0.1U_4
C202 0.1U_4
C203 0.1U_4
C205 0.1U_4
C206 0.1U_4
C208 0.1U_4
C210 0.1U_4
C214 0.1U_4
1.8VSUS
GMCH_CORE
C215 10U/6.3V/X5R
C218 10U/6.3V/X5R
C221 0.1U_4
C223 0.1U_4
C227 0.1U_4
C229 0.1U_4
C231 0.1U_4
C233 0.1U_4
C235 0.1U_4
C237 0.1U_4
C241 0.1U_4
C243 0.1U_4
C246 0.1U_4
C248 0.1U_4
C250 0.1U_4
C253 0.1U_4
C254 0.1U_4
C255 0.1U_4
C256 0.1U_4
C257 0.1U_4
C258 0.1U_4
C259 0.1U_4
1.This part should not contain any substances which are specified in SS-00259-1
2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.
QUANTA COMPUTER
Title
Alviso POWER
Size
Document Number
RD1 Main Board
Rev
1A
Date: Saturday, June 25, 2005
Sheet 8 of 32





Close to PR-CONN



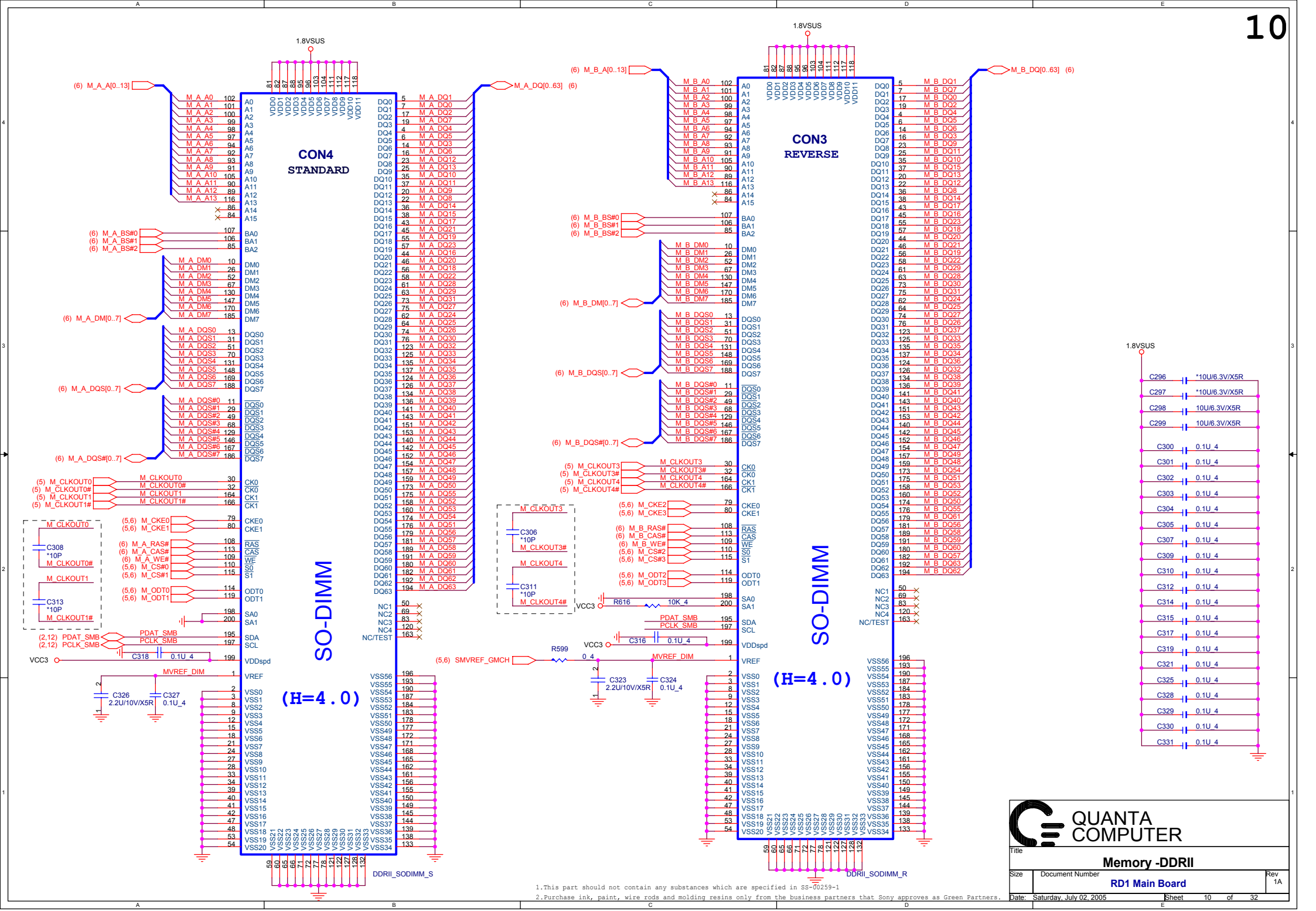
Plug Display true table

	DOCK_INSERT#	SENSE_EN#	MB CRT	DK_VGA_SENSE#	DOCK DVI	DOCK CRT	AV_SENSE	CRT_SENSE
					(1) XOR			XOR
			(2) XOR					
MB no CRT/AV	H	L	H	H	X	X	L	L
MB CRT Plug	H	L	L	H	X	X	L	H
MB AV-OUT Plug	H	L	H	H	X	X	H	H
MB CRT/AV-OUT Plug	H	L	L	H	X	X	H	L
DOCK No CRT/DVI /AV	L	L	H	H	L	H	L	L
DOCK CRT Plug	L	L	H	L	L	L	L	H
DOCK DVI Plug	L	L	H	L	H	H	L	H
DOCK AV Plug	L	L	H	H	L	H	H	H
DOCK CRT/DVI Plug	L	L	H	H	H	L	L	L
DOCK DVI/AV Plug	L	L	H	L	H	H	H	L
DOCK CRT/AV Plug	L	L	H	L	L	L	H	L
DOCK CRT/DVI/AV Plug	L	L	H	H	H	L	H	H
	X	H	X	X	X	X	X	L

1.This part should not contain any substances which are specified in SS-00259-1
2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

QUANTA COMPUTER

Title	CRT /AV OUT	
Size	Document Number: RD1 Main Board	Rev 1A
Date: Thursday, June 30, 2005	Sheet 9 of 32	



1.This part should not contain any substances which are specified in SS-00259-1

2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

QUANTA COMPUTER

Title	Memory -DDRII	
Size	Document Number: RD1 Main Board	Rev: 1A
Date: Saturday, July 02, 2005	Sheet 10 of 32	

U13A

RTC
CLK_32KX1 Y1 RTCX1
CLK_32KX2 Y2 RTCX2
-RTCRST AA2 RTCRST#
VCCRTC R213 1M_4 AA3 INTRUDER#
AA5 INTVRMEN

LPC
LAD0 P2 LPC_AD0 (20)
LAD1/FB1 N3 LPC_AD1 (20)
LAD2/FB2 N5 LPC_AD2 (20)
LAD3/FB3 N4 LPC_AD3 (20)
LDRQ0# N6 LPC_DRQ0#
LDRQ1#/GPI41 P4 LPC_DRQ1# LPC_DRQ1# (20)
LFRAME# P3 LPC_FRAME# (20)

VCC3
LPC_DRQ0# R211 10K_4
LPC_DRQ1# R212 10K_4
H_DPSLP# R214 *56_4 VCCP

CPU
VCCP R215 56_4
(3) H_FERR# R216 56_4
(3) H_NMI AE25 NMI
(3) H_A20M# AF23 A20M#
ICH_FERR# AF24 FERR#
(3) H_IGNNE# AG26 IGNNE#
(3) H_INTR AG24 INTR
(3) H_CPUINIT# AF27 INIT#
-RCIN AD23 RCIN#
(20) GATEA20 AF22 A20GATE

CPUPWRGD/GPO49 AG25 H_CPUPWRGD (3)
INIT3_3V# AE22 T58
THRMTRIP# AE23 R217 75/F_4 VCCP
SMI# AG27 H_SMI# (3)
STPCLK# AE26 H_STPCLK# (3)
CPUSLP# AE27 T130 H_DPSLP#
DPSLP#/TP[2] AD27 H_DPSLP# (3)
DPRSTP#/TP[4] AE24 T91

PCI
PCI_AD0 E2 AD0
PCI_AD1 E5 AD1
PCI_AD2 C2 AD2
PCI_AD3 F5 AD3
PCI_AD4 F3 AD4
PCI_AD5 E9 AD5
PCI_AD6 F2 AD6
PCI_AD7 D6 AD7
PCI_AD8 E6 AD8
PCI_AD9 D3 AD9
PCI_AD10 A2 AD10
PCI_AD11 D2 AD11
PCI_AD12 D5 AD12
PCI_AD13 H3 AD13
PCI_AD14 B4 AD14
PCI_AD15 J5 AD15
PCI_AD16 K2 AD16
PCI_AD17 K5 AD17
PCI_AD18 D4 AD18
PCI_AD19 L6 AD19
PCI_AD20 G3 AD20
PCI_AD21 H4 AD21
PCI_AD22 H2 AD22
PCI_AD23 H5 AD23
PCI_AD24 B3 AD24
PCI_AD25 M6 AD25
PCI_AD26 B2 AD26
PCI_AD27 K6 AD27
PCI_AD28 K3 AD28
PCI_AD29 A5 AD29
PCI_AD30 L1 AD30
PCI_AD31 K4 AD31
(14,17,19) PCI_AD[0..31]

C/BE0# J6 PCI_CBE#0 (14,17,19)
C/BE1# H6 PCI_CBE#1 (14,17,19)
C/BE2# G4 PCI_CBE#2 (14,17,19)
C/BE3# G2 PCI_CBE#3 (14,17,19)

FRAME# J3 PCI_FRAME# PCI_FRAME# (14,17,19)
IRDY# A3 PCI_IRDY# PCI_IRDY# (14,17,19)
TRDY# J2 PCI_TRDY# PCI_TRDY# (14,17,19)
DEVSEL# C3 PCI_DEVSEL# PCI_DEVSEL# (14,17,19)
STOP# J1 PCI_STOP# PCI_STOP# (14,17,19)
PAR E1 PCI_PAR (14,17,19)
SERR# G5 PCI_SERR# PCI_SERR# (14,17,19)
PERR# E3 PCI_PERR# PCI_PERR# (14,17,19)
PLOCK# C5 PCI_LOCK#

REQ0# L5 PCI_REQ#0 PCI_REQ#0 (14)
REQ1# B5 PCI_REQ#1
REQ2# M5 PCI_REQ#2 PCI_REQ#2 (19)
REQ3# B8 PCI_REQ#3 PCI_REQ#3 (17)
REQ4#/GPI40 F7 PCI_REQ#4
REQ5#/GPI1 E8 PCI_REQ#5
REQ6#/GPI0 B7 PCI_REQ#6

GNT0# C1 PCI_GNT#0 PCI_GNT#0 (14)
GNT1# B6
GNT2# F1 PCI_GNT#2 PCI_GNT#2 (19)
GNT3# C8 PCI_GNT#3 PCI_GNT#3 (17)
GNT4#/GPO48 E7
GNT5#/GPO17 F6
GNT6#/GPO16 D8

PIRQA# N2 PCI_INTA# PCI_INTA# (14)
PIRQB# L2 PCI_INTB# PCI_INTB# (19)
PIRQC# M1 PCI_INTC# PCI_INTC# (17)
PIRQD# L3 PCI_INTD#
PIRQE#/GPI2 D9 PCI_INTE#
PIRQF#/GPI3 C7 PCI_INTF#
PIRQG#/GPI4 C6 PCI_INTG#
PIRQH#/GPI5 M3 PCI_INTH#

PCLK_ICH6
R437 *33
C617 *18P
(14,19) PCI_PME# P6 PME#
(2) PCLK_ICH6 G6 PCICLK
(14,16,17,19) PCI_RST# R2 PCIRST#
PLTRST1_# R5 PLTRST#
(17,19) PCI_CLKRUN# AF19 CLKRUN#/GPIO32
PCI_RST#: Only for PCI

IDE
PDD0 AD14 DD0
PDD1 AE15 DD1
PDD2 AF14 DD2
PDD3 AD12 DD3
PDD4 AE14 DD4
PDD5 AC11 DD5
PDD6 AD11 DD6
PDD7 AB11 DD7
PDD8 AE13 DD8
PDD9 AF13 DD9
PDD10 AB12 DD10
PDD11 AB13 DD11
PDD12 AC13 DD12
PDD13 AE15 DD13
PDD14 AG15 DD14
PDD15 AD13 DD15
(18) PDD[0..15] PDD[0..15]

PDCS1# AD16 DCS1#
PDCS3# AE17 DCS3#
PDA0 AC16 DA0
PDA1 AB17 DA1
PDA2 AC17 DA2
PDIOR# AE16 DIOR#
PDIOW# AC14 DIOW#
PIORDY AF16 IORDY
IRQ14 AB16 IDEIRQ
PDDREQ AB14 DDREQ
PDDACK# AB15 DDACK#
(18) PDDREQ PDDREQ
(18) PDIOW# PDIOW#
(18) PDIOR# PDIOR#
(18) PIORDY PIORDY
(18) PDDACK# PDDACK#
(18) IRQ14 IRQ14
(18) PDA0 PDA0
(18) PDA1 PDA1
(18) PDA2 PDA2
(18) PDCS1# PDCS1#
(18) PDCS3# PDCS3#
VCC3 R328 4.7K_4 PIORDY

SATA
SATALED# AC19 SATA_LED#
SATA0_RXN AE3 SATA_RXN0 (18)
SATA0_RXP AD3 SATA_RXP0 (18)
SATA0_TXN AG2 SATA_TXN0_C C335 3900PF SATA_TXN0 (18)
SATA0_TXP AF2 SATA_TXP0_C C336 3900PF SATA_TXP0 (18)
SATA2_RXN AD7
SATA2_RXP AC7
SATA2_TXN AF6
SATA2_TXP AG6
SATA_CLKN AC2 CLK_PCIE_SATA# (2)
SATA_CLKP AC1 CLK_PCIE_SATA (2)
SATARBIAS# AG11
SATARBIAS AF11 SATA_RBIAS_PN R224 24.9/F_4
Place within 500mils of ICH6 ball

AC-97/AZALIA
ACZ_BIT_CLK C10 AZ_BITCLK1 (17)
ACZ_SYNC B9 AZ_SYNC AZ_SYNC0 (21)
ACZ_RST# A10 AZ_RST# AZ_SYNC1 (17)
ACZ_SDIN0 F11 AZ_SDIN0 (21) AZ_RST#0 (21)
ACZ_SDIN1 F10 AZ_SDIN1 (17) AZ_RST#1 (17)
ACZ_SDIN2 B10 T61
ACZ_SDO C9 AZ_SDOUT AZ_SDOUT0 (21) AZ_SDOUT1 (17)
R482 39_4
R473 39_4
R225 39_4
R227 39_4
R228 39_4
R229 39_4
R230 39_4
R231 39_4

ICH6-M

RP15
PCI_INTE# PCI_REQ#3
PCI_REQ#5 PCI_REQ#6
PCI_INTF# PCI_REQ#1
PCI_INTG# PCI_IRDY#
VCC3 10P8R-8.2K

RP16
PCI_LOCK# PCI_DEVSEL#
PCI_REQ#4 PCI_PERR#
PCI_SERR# PCI_FRAME#
VCC3 PCI_TRDY#
10P8R-8.2K

RP17
PCI_REQ#0 PCI_STOP#
PCI_INTD# PCI_INTB#
PCI_REQ#2 PCI_INTC#
PCI_INTH# PCI_INTA#
VCC3 10P8R-8.2K

PCI_CLKRUN# R220 10K_4
-RCIN R221 10K_4
IRQ14 R438 8.2K_4

CLK_32KX1
R222 10M
CLK_32KX2 R223 0_4
Y2 32.768K/10PPM/12.5P
C333 18P
C334 18P

PCI_REQ0 : TI-7420
PCI_REQ1 : X
PCI_REQ2 : LAN (8100CL)
PCI_REQ3 : MINI PCI
PCI_REQ4 : X
PCI_REQ5 : X
PCI_REQ6 : X
IDSEL:(AD16 ~ 29)
AD24 : LAN(8100CL)
AD25 : TI-7420
AD26 : MINI PCI
.

PCI_INTA# : X
PCI_INTB# : LAN(8100CL)
PCI_INTC# : MINI PCI
PCI_INTD# : X
PCI_INTE# : TI-7420
PCI_INTF# : TI-7420
PCI_INTG# : TI-7420
PCI_INTH# : MINI PCI

RTC
5VPCU
R232 2.2K_4 RTC_N01
PMBT3904 Q12
R233 1K_4
VCCRTC1
3VPCU D4 CH501H-40
D5 CH501H-40
VCCRTC
R234 4.7K
RTC_N02
BT1 ML1220-SOCKET
G1 -RCRST
Power on delay 18~25 ms
R236 100K
C339 1U/10V/Y5V
C340 1U
R237 15K/F

3VSUS C337 0.1U_4
PLTRST1_# U14 TC7SH08FU
PLTRST# (3,5,12,18,32)
PLTRST# : LPC/PCIE/Others

VCC3 C338 0.1U_4
R235 10K_4
SATA_LED# U15 TC7SH08FU
-HDDLED (15)

AZ_SYNC R226 *1K_4 VCC3

1.This part should not contain any substances which are specified in SS-00259-1
2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

QUANTA COMPUTER

Title	ICH6-M	
Size	Document Number	Rev
	RD1 Main Board	1A
Date: Thursday, June 30, 2005	Sheet 11 of 32	

U13B

USB_PORT
USB_PORT
CAMERA
DOCKING_LPT

(21) USBP0+ D21 USBP0P
(21) USBP0- C21 USBP0N
(21) -USBOC0 C27 OC0#
(21) USBP2+ C19 USBP2P
(21) USBP2- D19 USBP2N
(21) -USBOC2 B26 OC2#
(21) USBP4+ D17 USBP4P
(21) USBP4- E17 USBP4N
-USBOC4 C23 OC4#/GPI9
(22) USBP6+ D15 USBP6P
(22) USBP6- C15 USBP6N
-USBOC6 C25 OC6#/GPI14

USB

USBP1P B20 USBP1+ (21)
USBP1N A20 USBP1- (21)
-USBOC1 B27 OC1# -USBOC1 (21)
USBP3P B18 USBP3+ (22)
USBP3N A18 USBP3- (22)
-USBOC3 C26 OC3#
USBP5P A16 USBP5+ (18)
USBP5N B16 USBP5- (18)
-USBOC5 D23 OC5#/GPI10
B14 USBP7P
A14 USBP7N
-LM86_ALERT (3) C24 OC7#/GPI15
B22 USBRBIAS
USBRBIAS A22 USBRBIAS# R246 22.6/F

USB_PORT
DOCKING_HUB
FeliCa
Reserve

RP39
-USBOC2 6
-USBOC1 7
-USBOC3 8
-USBOC0 9
10
RVCC3
5 -USBOC6
4
3 -USBOC4
2
1 -USBOC5
10P8R-8.2K

LCD ID
VCC3
LCDID2 RP18 7 8 10Kx4
LCDID3 5 6
LCDID4 3 4
LCDID5 1 2
LCDID1 R497 10K_4
LCDID0 R498 10K_4

SW1
LCDID0 1 12 LCDID0_R
LCDID1 2 11 LCDID1_R
LCDID2 3 10 LCDID2_R
LCDID3 4 9 LCDID3_R
LCDID4 5 8 LCDID4_R
LCDID5 6 7 LCDID5_R
HDS406-E
RP19 1KX4
LCDID3_R 7 8
LCDID2_R 5 6
LCDID1_R 3 4
LCDID0_R 1 2
R499 1K_4
R500 1K_4

CLK48_USB
R628 10_4
C1026 8.2P_4
(2) CLK48_USB CLK48_USB A27 CLK48

DMI

(5) DMI_RXN0 T25 DMI0_RXN
(5) DMI_RXP0 T24 DMI0_RXP
(5) DMI_TXN0 R27 DMI0_TXN
(5) DMI_TXP0 R26 DMI0_TXP
DMI2_RXN Y25
DMI2_RXP Y24
DMI2_TXN W27
DMI2_TXP W26
(5) DMI_RXN1 V25 DMI1_RXN
(5) DMI_RXP1 V24 DMI1_RXP
(5) DMI_TXN1 U27 DMI1_TXN
(5) DMI_TXP1 U26 DMI1_TXP
DMI3_RXN AB24
DMI3_RXP AB23
DMI3_TXN AA27
DMI3_TXP AA26
(2) CLK_PCIE_ICH# AD25 DMI_CLKN
(2) CLK_PCIE_ICH AC25 DMI_CLKP
F24 DMI_ZCOMP
DMI_IRCOMP F23 DMI_COMP R253 24.9/F_4 VCC1.5

M/B ID
VCC3
R247 10K_4 MBID0 R248 *10K_4
R249 *10K_4 MBID1 R250 10K_4
R251 *10K_4 MBID2 R252 10K_4

PCI-EXPRESS

H25 HSIN0
H24 HSIP0
G27 HSON0
G26 HSOP0
HSIN2 M25
HSIP2 M24
HSON2 L27
HSOP2 L26
K25 HSIN1
K24 HSIP1
J27 HSON1
J26 HSOP1
HSIN3 P24
HSIP3 P23
HSON3 N27
HSOP3 N26

RVCC3
-KBSMI R580 10K_4
-SCI R581 *10K_4
SMBALERT# R256 10K_4
SMLINK0 R257 10K_4
SMLINK1 R258 10K_4
SMB_LINK_ALERT# R259 10K_4
ICH_RI# R260 10K_4
-BATLOW R261 10K_4
PCIE_WAKE# R262 680_4

SM&SMI

(2,10) PCLK_SMB PCLK_SMB Y4 SMBCLK
(2,10) PDAT_SMB PDAT_SMB W5 SMBDATA
SMBALERT# W6 SMBALERT#/GPI11
SMLINK0 W4 SMLINK0
SMLINK1 U6 SMLINK1
LINKALERT# Y5 SMB_LINK_ALERT#

VCC3
SERIRQ R263 10K_4
MCH_SYNC# R264 10K_4
PCLK_SMB R254 2.2K_4
PDAT_SMB R255 2.2K_4

PM

ICH_RI# T2 RI#
THRM# AC20 THRM#
(7,20) PWROK PWROK AA1 PWROK
(23) DPRSLPVR DPRSLPVR AE20 DPRSLPVR/TP1
-BATLOW V2 BATLOW#/TP0
(20) -DNBSWON U1 PWRBTN#
(20) RSMRST# RSMRST# Y3 RSMRST#
(5,20) IMVP_PWG AF21 VRMPWRGD
(5) PM_BMBUSY# AD19 BM_BUSY#/GPIO6
T62 W3 SUS_STAT#/LPCPD#
T63 V6 SUSCLK
T4 SLP_S3#
T5 SLP_S4#
T6 SLP_S5#
V5 LAN_RST#
U2 SYS_RESET#
U5 WAKE# PCIE_WAKE#
AG21 MCH_SYNC#
AC21 STP_PCI#/GPO18
AD22 STP_CPU#/GPO20
AB20 SERIRQ SERIRQ
-SUSB (20)
-SUSC (20)
-SLP_S5 (20)
PLTRST# (3,5,11,18,32)
H_DBR# (3)
-STP_PCI (2)
-STP_CPU (2,23)
SERIRQ (14,20)

PWROK R265 10K_4
DPRSLPVR R266 *100K_4

MISC&GPIO

(2) 14M_ICH 14M_ICH E10 CLK14
(21) PCSPK F8 SPKR
AE19 GPI7
(20) -KBSMI -KBSMI R1 GPI8
M2 GPI12
(20) -SCI -SCI R6 GPI13
(7) DISP_ON AB21 GPO19
AD20 GPO21
AD21 GPO23
LCDID1 V3 GPIO24
P5 GPIO25
AF17 SATA0GP/GPIO26 LCDID4
R3 GPIO27 MBID0
T3 GPIO28 MBID1
AE18 SATA1GP/GPIO29 MBID2
AF18 SATA2GP/GPIO30 LCDID0
AG18 SATA3GP/GPIO31 LCDID5
AF20 GPIO33 LCDID2
AC18 GPIO34 LCDID3

THRM# R555 10K_4 3VSUS
H_DBR# R270 150/F_4 RVCC3

14M_ICH
R655 *10_4
C1058 *10P_4

LAN

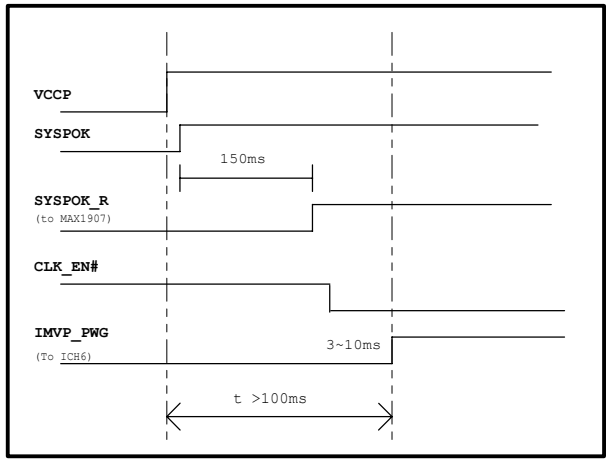
D12 EE_CS
B12 EE_SHCLK
D11 EE_DOUT
F13 EE_DIN
LAN_RXD0 E12
LAN_RXD1 E11
LAN_RXD2 C13
LAN_TXD0 C12
LAN_TXD1 C11
LAN_TXD2 E13
LAN_CLK F12
LAN_RSTSYNC B11

RESERVED

AC5 RSVD1
AD5 RSVD2
AF4 RSVD3
AG4 RSVD4
AC9 RSVD5
RSVD6 AD9
RSVD7 AF8
RSVD8 AG8
RSVD9 U3

ICH6-M

Reserve AC termination for EMI



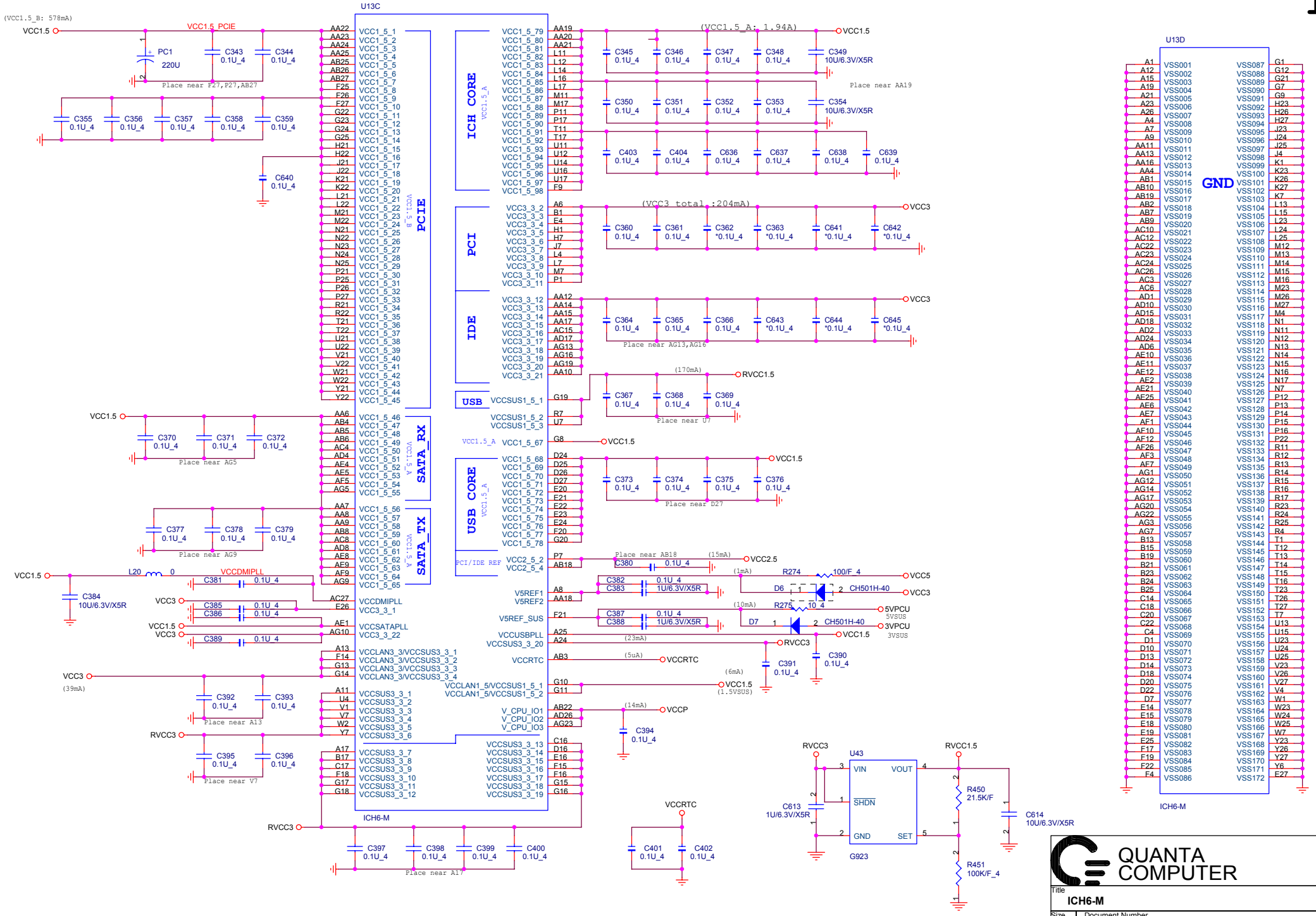
RVCC3
C341 *0.1U_4
RVCC3
R271 *100K_4
-RSMRST-1
C342 *0.1U_4
U16 *NC7SZ58 AL07SZ58001
RSMRST# Min. 5ms
R272 10K_4

(25) SYSPOK R626 0_4 SYSPOK_R (23)

1.This part should not contain any substances which are specified in SS-00259-1
2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

QUANTA COMPUTER

Title: ICH6-M
Size: Document Number: RD1 Main Board Rev: 1A
Date: Thursday, June 30, 2005 Sheet 12 of 32

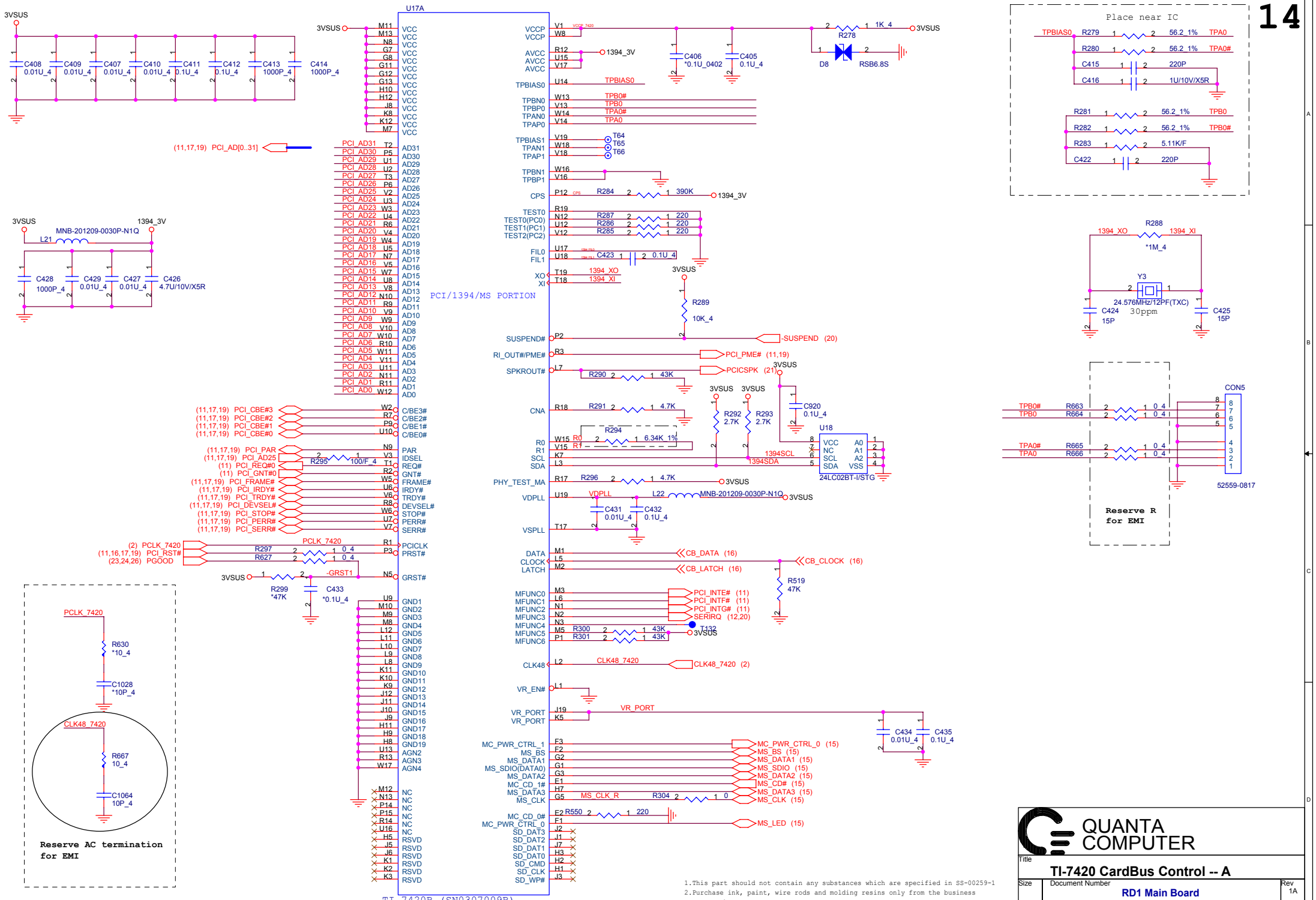


1.This part should not contain any substances which are specified in SS-00259-1

2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

QUANTA COMPUTER

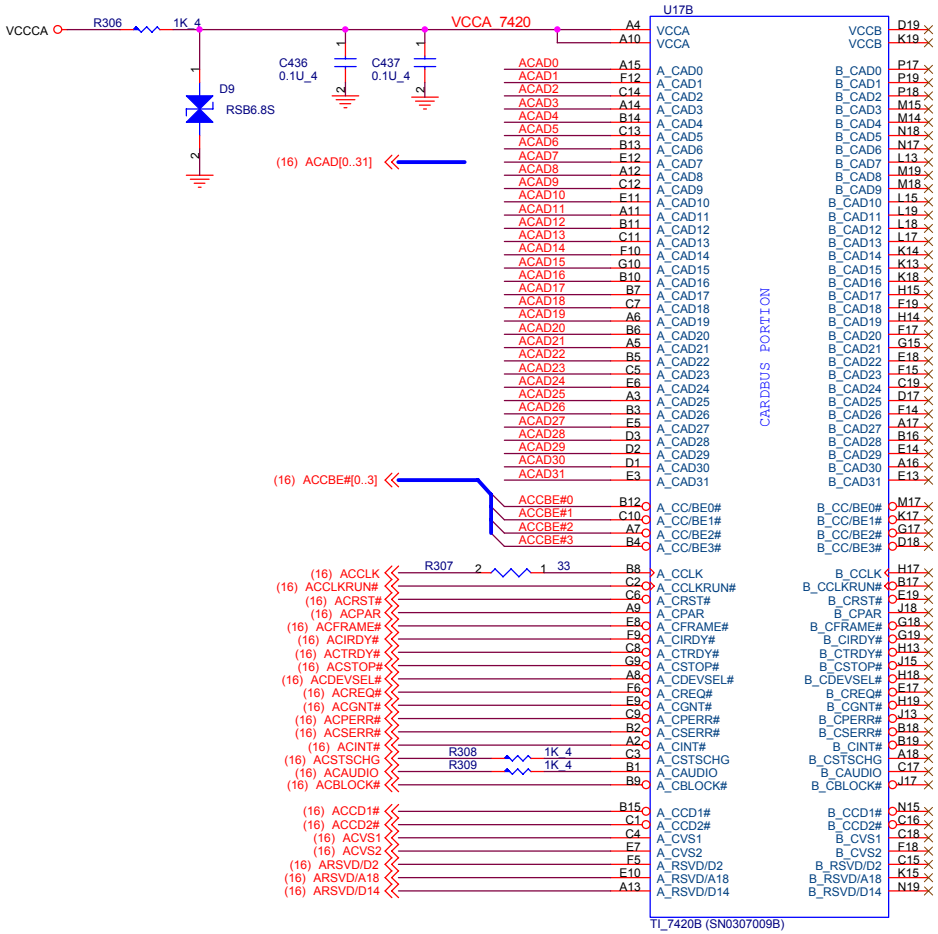
Title	ICH6-M	
Size	Document Number: RD1 Main Board	Rev 1A
Date: Saturday, June 25, 2005	Sheet 13 of 32	

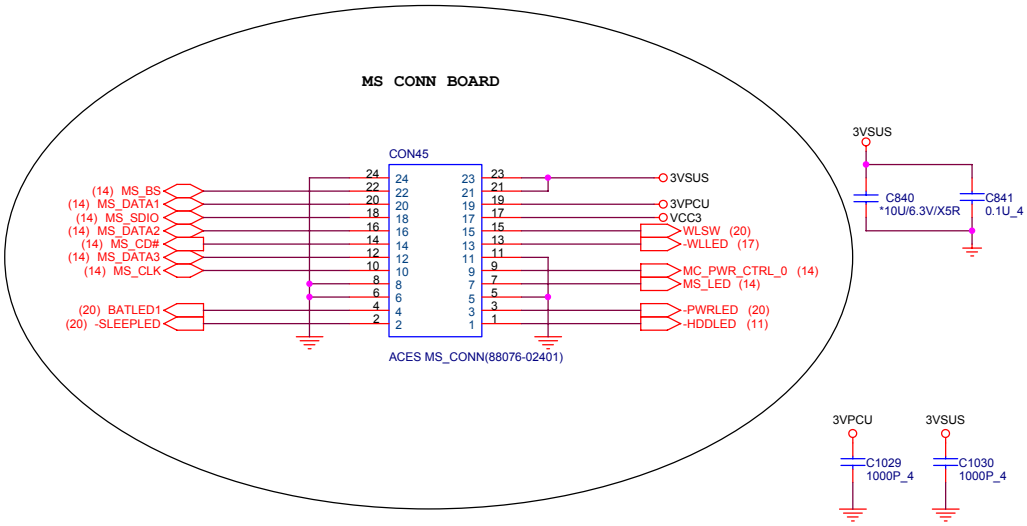

U17A
TI_7420B (SN0307009B)
PCI/1394/MS PORTION
Reserve AC termination for EMI
Reserve R for EMI
Place near IC
24LC02BT-I/STG
24.576MHz/12PF(TXC)
52559-0817
1. This part should not contain any substances which are specified in SS-00259-1
2. Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

Title	TI-7420 CardBus Control -- A
Size	Document Number: RD1 Main Board
Rev	1A
Date:	Saturday, July 02, 2005
Sheet	14 of 32

QUANTA COMPUTER

MB side : ACES 88076-02401 with boss
MS side : ACES 88066-02401 with boss
H=2.0mm





1.This part should not contain any substances which are specified in SS-00259-1
2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

QUANTA COMPUTER

Title	TI-7420B CardBus Control -- B	
Size	Document Number: RD1 Main Board	Rev: 1A
Date: Saturday, July 02, 2005	Sheet 15 of 32	

(15) ACAD[0..31]
(15) ACCBE#[0..3]

PCMCIA SOCKET

CON7

	1	GND	
(15) ACAD0	2	D3 - CAD0	
(15) ACAD1	3	D4 - CAD1	
(15) ACAD3	4	D5 - CAD3	
(15) ACAD5	5	D6 - CAD5	
(15) ACAD7	6	D7 - CAD7	
(15) ACCBE#0	7	CE1- CCBE0	
(15) ACAD9	8	A10- CAD9	
(15) ACAD11	9	OE - CAD11	
(15) ACAD12	10	A11- CAD12	
(15) ACAD14	11	A9 - CAD14	
(15) ACCBE#1	12	A8 - CCBE1	
(15) ACPAR	13	A13- CPAR	
(15) ACPERR#	14	A14- CPERR	
(15) ACGNT#	15	WE/PGM - CGNT	
(15) ACINT#	16	RDY/BSY,IRQ*INT	
VCCCA	17	VCC	
VPPCA	18	VPP1	
(15) ACCLK	19	A16- CCLK	
(15) ACIRDY#	20	A15- CIRDY	
(15) ACCBE#2	21	A12- CCBE2	
(15) ACAD18	22	A7 - CAD18	
(15) ACAD20	23	A6 - CAD20	
(15) ACAD21	24	A5 - CAD21	
(15) ACAD22	25	A4 - CAD22	
(15) ACAD23	26	A3 - CAD23	
(15) ACAD24	27	A2 - CAD24	
(15) ACAD25	28	A1 - CAD25	
(15) ACAD26	29	A0 - CAD26	
(15) ACAD27	30	D0 - CAD27	
(15) ACAD29	31	D1 - CAD29	
(15) ARSVD/D2	32	D2 - RFU	
(15) ACCLKRUN#	33	WP,IOIS16-CKRUN	
	34	GND	
	35	GND	
A_CCD1#_C	36	CD1- CCD1	
(15) ACAD2	37	D11- CAD2	
(15) ACAD4	38	D12- CAD4	
(15) ACAD6	39	D13- CAD6	
(15) ARSVD/D14	40	D14- RFU	
(15) ACAD8	41	D15- CAD8	
(15) ACAD10	42	CE2- CAD10	
(15) ACVS1	43	RFSH,VS*1-CVS1	
(15) ACAD13	44	IORD-CAD13	
(15) ACAD15	45	IOWR-CAD15	
(15) ACAD16	46	A17- CAD16	
(15) ARSVD/A18	47	A18- RFU	
(15) ACBLOCK#	48	A19- CBLOCK	
(15) ACSTOP#	49	A20- CSTOP	
(15) ACDEVSEL#	50	A21- CDEVSEL	
VCCCA	51	VCC	
VPPCA	52	VPP2	
(15) ACTRDY#	53	A22- CTRDY	
(15) ACFRAME#	54	A23- CFRAME	
(15) ACAD17	55	A24- CAD17	
(15) ACAD19	56	A25- CAD19	
(15) ACVS2	57	NC - CVS2	
(15) ACRST#	58	RESET-CRST	
(15) ACSERR#	59	WAIT-CSERR	
(15) ACREQ#	60	INPACK-CREQ	
(15) ACCBE#3	61	REG- CCBE3	
(15) ACAUDIO	62	BVD2,SP-CAUDIO	
(15) ACSTSCHG	63	BVD1,STSCHG-C*	
(15) ACAD28	64	D8 - CAD28	
(15) ACAD30	65	D9 - CAD30	
(15) ACAD31	66	D10- CAD31	
A_CCD2#_C	67	CD2- CCD2	
	68	GND	
	69	GND	
	70	GND	
	71	GND	
	72	GND	
	73	NC	
	74	NC	

SANTA-130660-1-68P

A_CCD1#_C — R601 22_4 — C845 270P — ACCD1# (15)

A_CCD2#_C — R602 22_4 — C849 270P — ACCD2# (15)

CardBus inject : 130660-1 , FB0A6063017
CardBus CONN : 130610-1

U19 TPS2220A

(14) CB_DATA	3	DATA	12V 7, 12V 20
(14) CB_CLOCK	4	CLOCK	
(14) CB_LATCH	5	LATCH	
-SUSRST	12	RESET#	
	15	OC#	3.3V 13
3VSUS	21	SHDN#	5V 1, 5V 2, GND 11
VPPCA	8	AVPP	
	9	AVCC	
	10	AVCC	
			NC 6, 14, 16, 17, 18, 19, 22, 23, 24

3VSUS — R310 10K_4

VCCCA — C440 0.1U_4, C441 0.1U_4, C442 22U/16V/X5R, D10 RSB6.8S

3VSUS — C438 1U/10V/Y5V

5VSUS — C439 1U/10V/Y5V

(20) PCMRSTEN — Q13 DTC144EU — R314 10K_4 — -SUSRST

(11,14,17,19) PCI_RST#

VPPCA — C444 0.1U_4, R312 100K_4

H1 H-1D148P2-RD1	H2 H-2D148P2-RD1	H3 H-3D100P2-RD1	H4 H-4D100P2-RD1	H5 H-5D91P2-RD1			
H12 H-C276D100P2	H13 H-C276D100P2	H14 H-C276D100P2	H15 H-C276D100P2	H11 H-C276D100P2	H17 H-C276D100P2	H18 H-C276i206d148p2	H20 H-C237i206d148p2
H16 H-C276D100P2	H19 H-C276i206d148p2	H21 H-C237i206d148p2	H22 H-C276D100P2	H23 H-C237D148P2	H24 H-C276D120P2	H25 H-C197D100P2	H26 H-C237DO124X85P2
H27 H-C276D100P2	H28 H-C276D100P2						

1.This part should not contain any substances which are specified in SS-00259-1
2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

QUANTA COMPUTER

Title: **CARDBUS SLOT**

Size | Document Number: **RD1 Main Board** | Rev: 1A

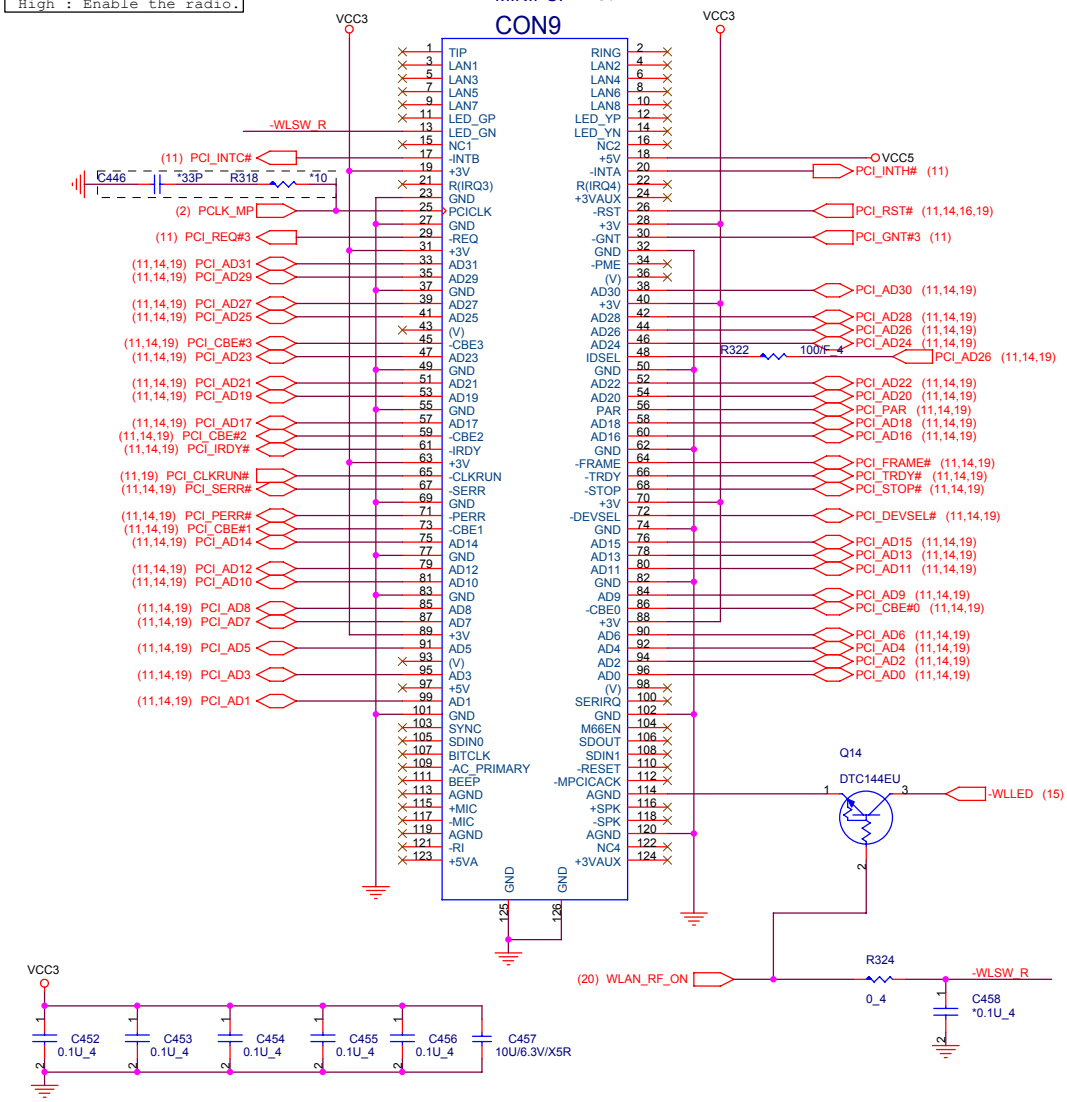
Date: Saturday, July 02, 2005 | Sheet 16 of 32

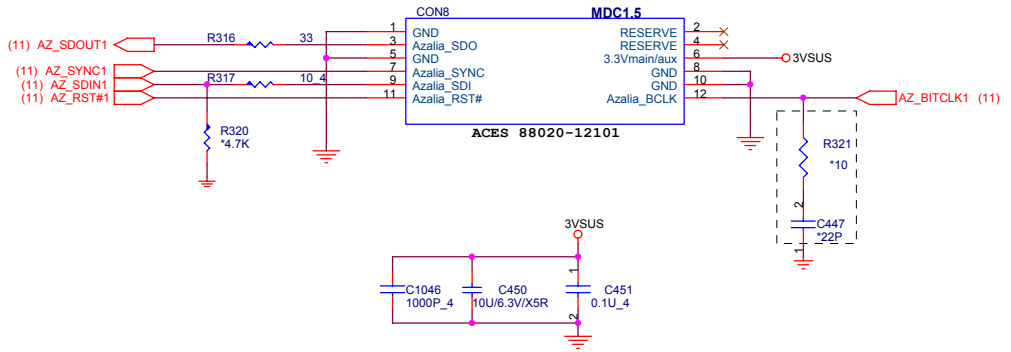
MDC V1.5

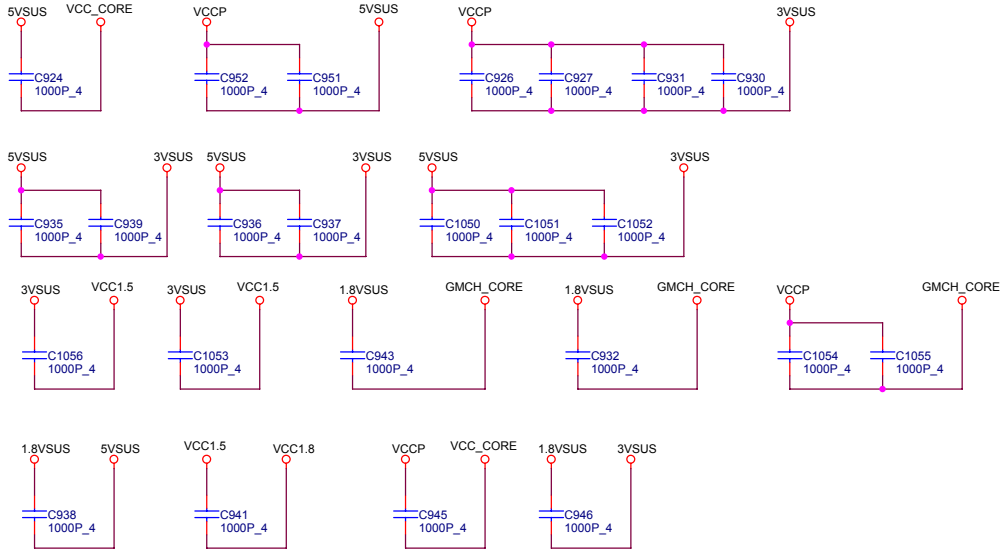
LED_GP :LED WLEN LINK
Output Current : 7mA
Blink rate : 1 flash per every 3 sec.

WLSW:
Low : disable the radio.
High : Enable the radio.

AMP 1734835-2
MINIPCI H=6.7
CON9





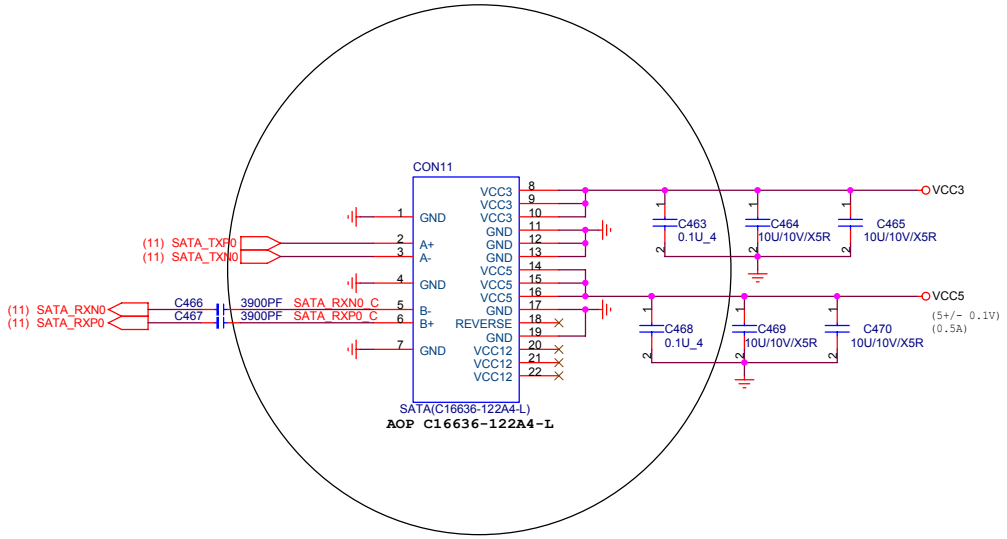


1.This part should not contain any substances which are specified in SS-00259-1
2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

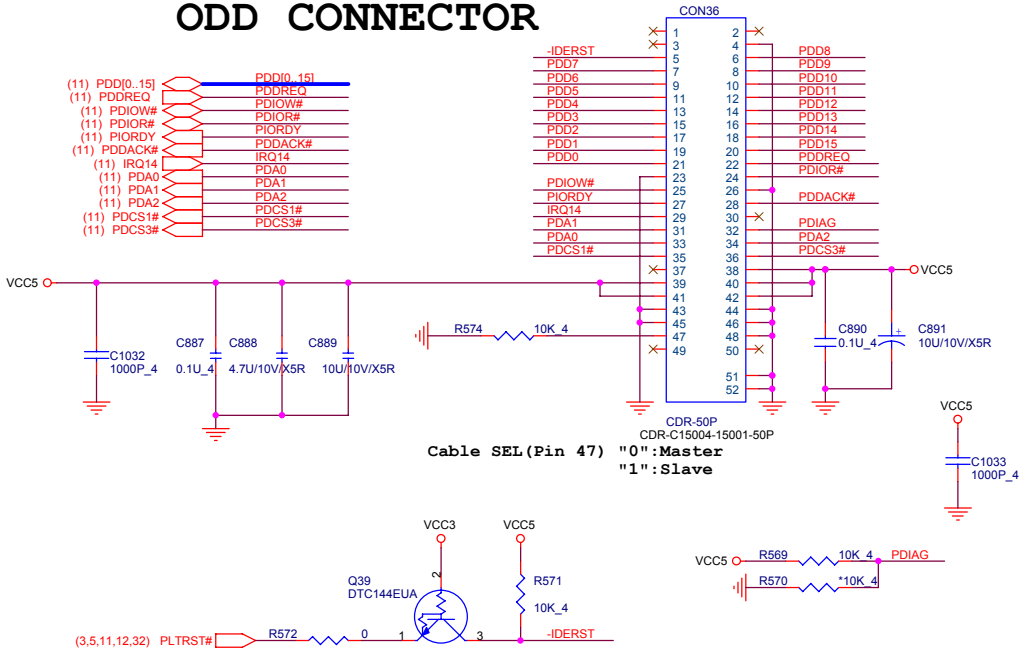
QUANTA COMPUTER

Title	MINIPCI /MDC	
Size	Document Number: RD1 Main Board	Rev 1A
Date: Thursday, June 30, 2005	Sheet 17 of 32	

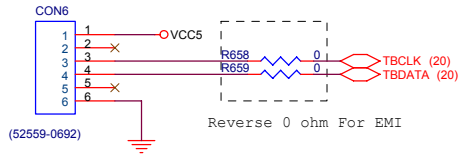
HDD CONNECTOR

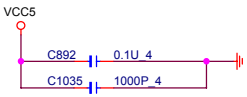


ODD CONNECTOR

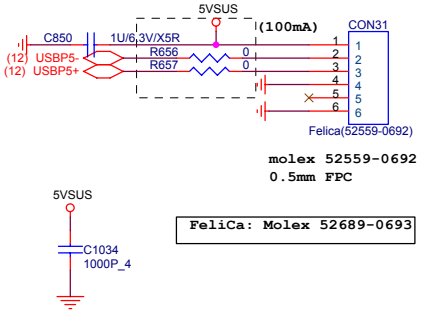


TOUCH PAD CONNECTOR





FeliCa



1.This part should not contain any substances which are specified in SS-00259-1
2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

QUANTA COMPUTER

Title	HDD/ODD/TP/FeliCa	
Size	Document Number: RD1 Main Board	Rev 1A
Date: Saturday, July 02, 2005	Sheet 18 of 32	

RTL8100CL

U21 — PQFP128A_0.5MM

Pin	Net
31	PMEB
105	LANWAKE
104	AD0
103	AD1
102	AD2
98	AD3
97	AD4
96	AD5
95	AD6
93	AD7
90	AD8
89	AD9
87	AD10
86	AD11
85	AD12
83	AD13
82	AD14
79	AD15
59	AD16
58	AD17
57	AD18
55	AD19
53	AD20
50	AD21
49	AD22
47	AD23
43	AD24
42	AD25
40	AD26
39	AD27
37	AD28
36	AD29
34	AD30
33	AD31
92	CBEB0
77	CBEB1
60	CBEB2
44	CBEB3
28	PCICLK
65	CLKRUNB
68	DEVSELB
61	FRAMEB
29	GNTB
30	REQB
46	IDSEL
25	INTAB
63	IRDYB
67	TRDYB
76	PAR
70	PERRB
75	SERRB
69	STOPB
27	PCIRSTB
23	ISOLATEB
106	EECS
111	EESK
109	AUX/EEDI
108	EEDO
1	TX+
2	TX-
5	RX+
6	RX-
121	XTAL1
122	XTAL2
8	CTRL25
127	RSET
117	LED0
115	LED1
114	LED2
12	AVDD25
32	VDD25_0
54	VDD25_1
78	VDD25_2
99	VDD25_3
26	VDD33_0
41	VDD33_1
56	VDD33_2
71	VDD33_3
84	VDD33_4
94	VDD33_5
107	VDD33_6
3	AVDD33_0
7	AVDD33_1
20	AVDD33(REG)
35	GND_0
52	GND_1
80	GND_2
100	GND_3
21	GND_4
38	GND_5
51	GND_6
66	GND_7
81	GND_8
91	GND_9
101	GND_10
119	GND_11
17	GND_12
4	GND_13
123	GND_14
124	GND_15
128	GND_16

U58 — 93LC46B-I/SNG

Pin	Net
1	CS
2	SK
3	DI
4	DO
8	VCC
7	NC
6	ORG
5	GND

U45

Pin	Net
4	RD+
7	RD-
9	TD+
12	TD-
16	VCC
8	GND
2	IA0
3	IA1
5	IB0
6	IB1
11	1C0
10	IC1
14	ID0
13	ID1
1	SEL /E
15	

PI3L110

Vender : PERICOM

U23 — 13F-38TK

Pin	Net
3	TDC
1	TD+
2	TD-
14	CT
16	TX+
15	TX-
7	RD+
8	RD-
6	RDC
10	RX+
9	RX-
11	CT

CON46 — LAN(DF19G-8P-1H08)

Pin	Net
1	TDN
2	TDP
3	
4	
5	RDN
6	RDP
7	
8	

Ref	Value
R489	3.6K
R487	10K_4
R353	49.9/F_4
R345	49.9/F_4
R444	5.2K/F
R486	100/F_4
R504	1K_4
R578	15K/F
R635	*33
C1036	*18P
R346	*0
R347	49.9/F_4
R348	49.9/F_4
R349	75/F_4
R350	75/F_4
R351	75/F_4
R352	75/F_4
R582	0_4
R363	10K_4
C499	0.1U_4
C500	0.1U_4
C471	22P
C472	22P
Y4	25MHz/20pF/20ppm
C485	0.1U_4
C483	0.1U_4
C484	0.1U_4
C486	0.1U_4
C488	0.1U_4
C487	22U/10V/Y5V
C490	0.1U_4
C491	0.1U_4
C493	0.1U_4
C495	0.1U_4
C492	0.1U_4
C494	4.7U/10V/X5R
C498	0.1U_4
C496	0.1U_4
C497	0.1U_4
C501	0.1U_4
C502	1000P/3KV
C503	0.1U_4
C504	5P
C505	0.1U_4
Q37	2SB1197K

SEL : L -> PR
H -> MB

1.This part should not contain any substances which are specified in SS-00259-1
2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

QUANTA COMPUTER

Title	LAN Relteck 8100CL
Size	Document Number: RD1 Main Board
Rev	1A
Date:	Saturday, July 02, 2005
Sheet	19 of 32

U24

Host interface: SERIRQ, LDRQ, LFRAME, LAD0, LAD1, LAD2, LAD3, LCLK, LRESET, SMI, PWUREQ, IOPD3/ECSCI, GA20/IOPB5, KBRST/IOPB6

AD Input: AD0 PCU_ID, AD1 MBATV, AD2 ISENS_IN (28), AD3, IOPE0AD4 -BAT_PRS (29), IOPE1/AD5 -SLP_S5 (12), IOPE2/AD6 -SUSC (12), IOPE3/AD7 HWPG (23), DP/AD8, DN/AD9

DA output: DA0 BRIGHT (7), DA1 CAMERA_EN (21), DA2 VFAN (21), DA3

PWM or PORT-A: IOPA0/PWM0 DOCK_INSERT# (9,19,22), IOPA1/PWM1 VOLMUTE (21), IOPA2/PWM2 AV_SENSE (9,21), IOPA3/PWM3 SENSE_EN# (9,22), IOPA4/PWM4 MAINON (4,6,26,27,28,30), IOPA5/PWM5 SUSON (6,25,26,27), IOPA6/PWM6 SLEEPLED (15), IOPA7/PWM7 RVCC_ON (24,27)

PORT-B: IOPB0/URXD BATLED1 (15), IOPB1/UTXD, IOPB2/USCLK -PWRLED (15), IOPB3/SCL1 MBCLK (3,29), IOPB4/SDA1 MBDATA (3,29), IOPB7/RING/PFAIL

PORT-C: IOPC0 -SUSPEND (14), IOPC1/SCL2, IOPC2/SDA2 VRON (23,25), IOPC3/TA1 -DNBSWON (12), IOPC4/TB1/EXWINT22, IOPC5/TA2, IOPC6/TB2/EXWINT23, IOPC7/CLKOUT PWROK (7,12)

PORT-D-1: IOPD0/RI1/EXWINT20 -HOLD, IOPD1/RI2/EXWINT21 ACIN, IOPD2/EXWINT24 -CAPSLED (21)

PORT-E: IOPE4/SWIN -NBSWON (21), IOPE5/EXWINT40 -SUSB (12), IOPE6/LPCPD/EXWIN45, IOPE7/CLKRUN/EXWINT46 CRT_SENSE (9)

PORT-I: IOPI0/D0 BD0, IOPI1/D1 BD1, IOPI2/D2 BD2, IOPI3/D3 BD3, IOPI4/D4 BD4, IOPI5/D5 BD5, IOPI6/D6 BD6, IOPI7/D7 BD7

PORT-H: IOPH0/A0/ENV0 BA0, IOPH1/A1/ENV1 BA1, IOPH2/A2/BADDR0 BA2, IOPH3/A3/BADDR1 BA3, IOPH4/A4/TRIS BA4, IOPH5/A5/SHBM BA5, IOPH6/A6 BA6, IOPH7/A7 BA7

BIOS

PORT-K: IOPK0/A8 BA8, IOPK1/A9 BA9, IOPK2/A10 BA10, IOPK3/A11 BA11, IOPK4/A12 BA12, IOPK5/A13/BE0 BA13, IOPK6/A14/BE1 BA14, IOPK7/A15/CBRD BA15

PORT-L: IOPL0/A16 BA16, IOPL1/A17 BA17, IOPL2/A18 BA18, IOPL3/A19, IOPL4/WR1 R671 T75 A_MUTE (21)

PORT-J-1: SEL0, IOPJ0/RD -CS, IOPJ1/WR0 -RD, -WR

PORT-J-2: IOPJ2/BST0 (21) S1_BUTTON#, IOPJ3/BST1 (21) S2_BUTTON#, IOPJ4/BST2, IOPJ5/PFS (22) DOCK_PGOOD, IOPJ6/PLI R647 100K_4, IOPJ7/BRKL_RSTO

PORT-M: IOPM0/D8 (15) WLSW, IOPM1/D9 (22) DOCK_PWON, IOPM2/D10 (12) RSMRST# R556, IOPM3/D11, IOPM4/D12, IOPM5/D13, IOPM6/D14, IOPM7/D15 (17) WLAN_RF_ON

PORT-D-2: IOPD4 PCMRSTEN (16), IOPD5, IOPD6 DC/-C (28,31), IOPD7 -CHGEN (28)

Key matrix scan: KBSIN0–KBSIN7, KBSOUT0–KBSOUT15

JTAG debug port: TINT, TCK, TDO, TDI, TMS — JTAG DEBUG PORT T69–T73

PS2 interface: PSCLK1/IOPF0, PSDAT1/IOPF1, PSCLK2/IOPF2, PSDAT2/IOPF3, PSCLK3/IOPF4, PSDAT3/IOPF5, PSCLK4/IOPF6 (21) -SCROLED, PSDAT4/IOPF7 (21) -NUMLED

32.768K/10PPM: 32KX1/32KCLKOUT, 32KX2

Input Voltage Range : 0 ~ 2.97 V

DVT : For TV SENSE
AV_SENSE 10K_4 R670 3VSUS
H: plug in L:plug out

For BIOS DEBUG: T68 10K_4 R375 3VPCU

DVT : Reverse for S4 wake up
Q23 PDTA124EU; -NBSWON, ACIN (31), -SUSB (12), -LID; D24 -HOLD, D25, D42; R378 100K_4

DVT : To solve pop noise

U50 PMC_pm39LV040-70JC
BA0–BA18, CE#, OE#, WE#, VPP, VCC, GND; D0–D7 BD0–BD7; -CS, -RD, -WR

Q34 2N7002E TBCLK_551 TBCLK (18)
Q35 2N7002E TBDATA_551 TBDATA (18)

I/O Address

BADDR1-0	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	(HCFGBAH, HCFGBAL)	(HCFGBAH, HCFGBAL)+1
1 1	Reserved	

BA2 : BADDR0
BA3 : BADDR1

Enviroment	ENV0 (BA0)	ENV1 (BA1)	TRIS (BA4)
IRE	0	0	0
OBD	0	1	0
DEV	1	0	0
PROG	1	1	0

BA0 : ENV0
BA1 : ENV1
BA4 : TRIS (If = 1 will tristate all i/o pins)
BA5 : SHBM(If = 1 Enable share host BIOS memory)

RP20 4.7Kx4: TBCLK, TBDATA, VCC5, MBDATA, MBCLK

U25 G923: 5VPCU, 3VPCU, VIN, VOUT, SHDN, GND, SET; C522 1U/6.3V/X5R, C523 10U/6.3V/X5R

Pin 94 if no use, pin 94(DN) should connect to ground
Pin 24 if no pull-high, will can't reboot.
Pin 103 internal is "A19",Can't use to GPIO
Pin 168 default is high
-DRQ0(pin 8) internal is no use

1.This part should not contain any substances which are specified in SS-00259-1
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QUANTA COMPUTER

Title	PC87591 & FLASH	
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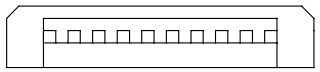
KEYBOARD



CON16

KB7641 series

Molex 52610-2490

To AUDIO-USB BOARD CONN.

CON42

HRS(50P)

USB POWER SW

U30 TPS2062

USB POWER SW

U29 TPS2062

DVT : For EMI

FAN

U59 G993

CON43 FAN_53261-0310

USB Camera

CON27 Camera(53398-0571)

Molex 53398-0571

CON48 E&T-3702Y-T004-NNN

MIC CONN

CON47 MIC-CON(53398-0410)

DVT : Add MIC CONN on MB

CON44 020133MR004S520ZL

POWER SWITCH BOARD CONNECTOR

CON33 87151-2607-26P

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QUANTA COMPUTER

Title	K/B ,USB ,PS	
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DC power to NB side

FDS4435_NL/SI4835BDY
PQ559

VA+
PQ560B
IMD2AT108
PC591
0.1U/50V/X7R
PR160
220K
PC592
0.1U/50V/X7R
PR161
220K
DOCK_INSERT#
PQ560A
IMD2AT108
PC593
0.1U_0402

DC power from DOCKING side

VA+IN

Port Replicator CONN

CON25

DOCKING_LPT
(12) USBP6+
(12) USBP6-
DOCKING_HUB
(12) USBP3+
(12) USBP3-
(7,9) CRT_B
(7,9) CRT_R
(7,9) CRT_G
(7,9) HSYNC
(7,9) VSYNC
(7,9) DDC2BC
(7,9) DDC2BD
(9,20) SENSE_EN#
(9) DK_VGA_SENSE#
(20) DOCK_PWON
(20) DOCK_PGOOD
PROTECT_R

DOCK_INSERT#
DVI_TX0- (32)
DVI_TX0+ (32)
DVI_CLK- (32)
DVI_CLK+ (32)
DVI_TX2- (32)
DVI_TX2+ (32)
DVI_TX1- (32)
DVI_TX1+ (32)
DVI_SDAT (32)
DVI_SCLK (32)
PR_RD+ (19)
PR_RD- (19)
PR_TD+ (19)
PR_TD- (19)
DVI-HPD (32)
DOCK_INSERT#

HRS EX80-54S

VA+IN
C1045
1000P_4

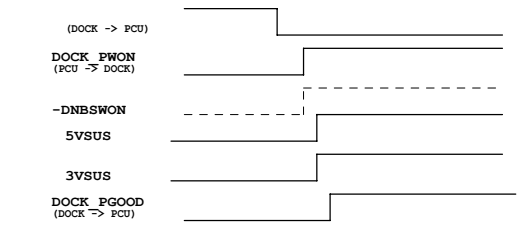
3VSUS
R428
10K_4
C611
0.1U_4

DOCK_INSERT# (9,19,20)

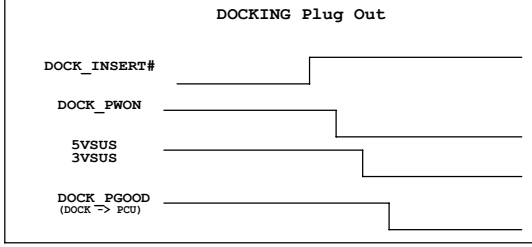
Docking detect Pin : PIN3,PIN30 and PIN 54

PROTECT_R R523
1K_4
D41
SW1010CPT
PROTECT (23,29,30,31)
C953
1U/6.3V

DOCKING Plug



DOCKING Plug Out



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QUANTA COMPUTER

Title	Docking	
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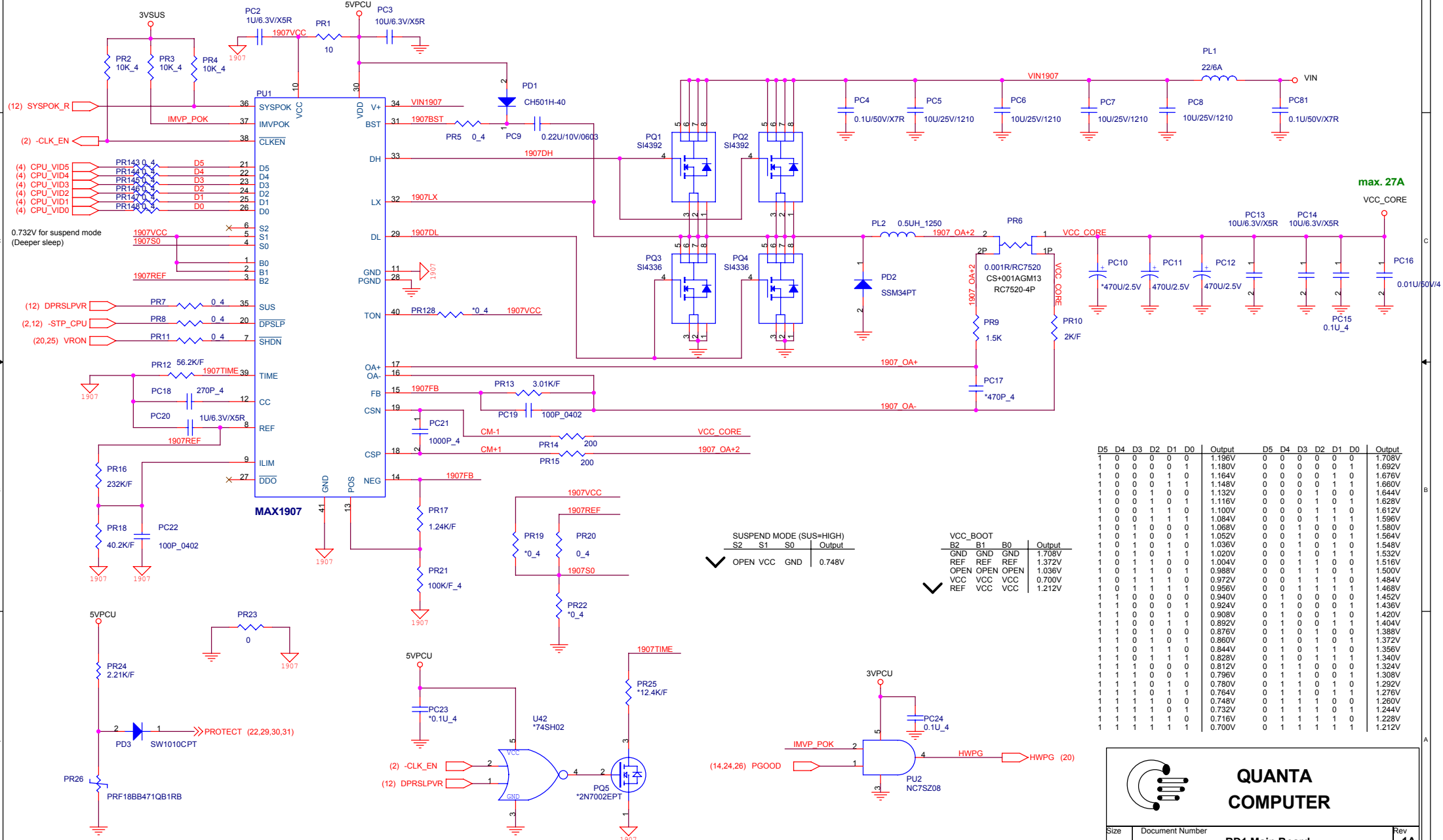
CPU CORE



SUSPEND MODE (SUS=HIGH)

S2	S1	S0	Output
OPEN	VCC	GND	0.748V

VCC_BOOT

B2	B1	B0	Output
GND	GND	GND	1.708V
REF	REF	REF	1.372V
OPEN	OPEN	OPEN	1.036V
VCC	VCC	VCC	0.700V
REF	VCC	VCC	1.212V

D5	D4	D3	D2	D1	D0	Output	D5	D4	D3	D2	D1	D0	Output
1	0	0	0	0	0	1.196V	0	0	0	0	0	0	1.708V
1	0	0	0	0	1	1.180V	0	0	0	0	0	1	1.692V
1	0	0	0	1	0	1.164V	0	0	0	0	1	0	1.676V
1	0	0	0	1	1	1.148V	0	0	0	0	1	1	1.660V
1	0	0	1	0	0	1.132V	0	0	0	1	0	0	1.644V
1	0	0	1	0	1	1.116V	0	0	0	1	0	1	1.628V
1	0	0	1	1	0	1.100V	0	0	0	1	1	0	1.612V
1	0	0	1	1	1	1.084V	0	0	0	1	1	1	1.596V
1	0	1	0	0	0	1.068V	0	0	1	0	0	0	1.580V
1	0	1	0	0	1	1.052V	0	0	1	0	0	1	1.564V
1	0	1	0	1	0	1.036V	0	0	1	0	1	0	1.548V
1	0	1	0	1	1	1.020V	0	0	1	0	1	1	1.532V
1	0	1	1	0	0	1.004V	0	0	1	1	0	0	1.516V
1	0	1	1	0	1	0.988V	0	0	1	1	0	1	1.500V
1	0	1	1	1	0	0.972V	0	0	1	1	1	0	1.484V
1	0	1	1	1	1	0.956V	0	0	1	1	1	1	1.468V
1	1	0	0	0	0	0.940V	0	1	0	0	0	0	1.452V
1	1	0	0	0	1	0.924V	0	1	0	0	0	1	1.436V
1	1	0	0	1	0	0.908V	0	1	0	0	1	0	1.420V
1	1	0	0	1	1	0.892V	0	1	0	0	1	1	1.404V
1	1	0	1	0	0	0.876V	0	1	0	1	0	0	1.388V
1	1	0	1	0	1	0.860V	0	1	0	1	0	1	1.372V
1	1	0	1	1	0	0.844V	0	1	0	1	1	0	1.356V
1	1	0	1	1	1	0.828V	0	1	0	1	1	1	1.340V
1	1	1	0	0	0	0.812V	0	1	1	0	0	0	1.324V
1	1	1	0	0	1	0.796V	0	1	1	0	0	1	1.308V
1	1	1	0	1	0	0.780V	0	1	1	0	1	0	1.292V
1	1	1	0	1	1	0.764V	0	1	1	0	1	1	1.276V
1	1	1	1	0	0	0.748V	0	1	1	1	0	0	1.260V
1	1	1	1	0	1	0.732V	0	1	1	1	0	1	1.244V
1	1	1	1	1	0	0.716V	0	1	1	1	1	0	1.228V
1	1	1	1	1	1	0.700V	0	1	1	1	1	1	1.212V

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QUANTA COMPUTER

Size	Document Number	Rev
	RD1 Main Board	1A
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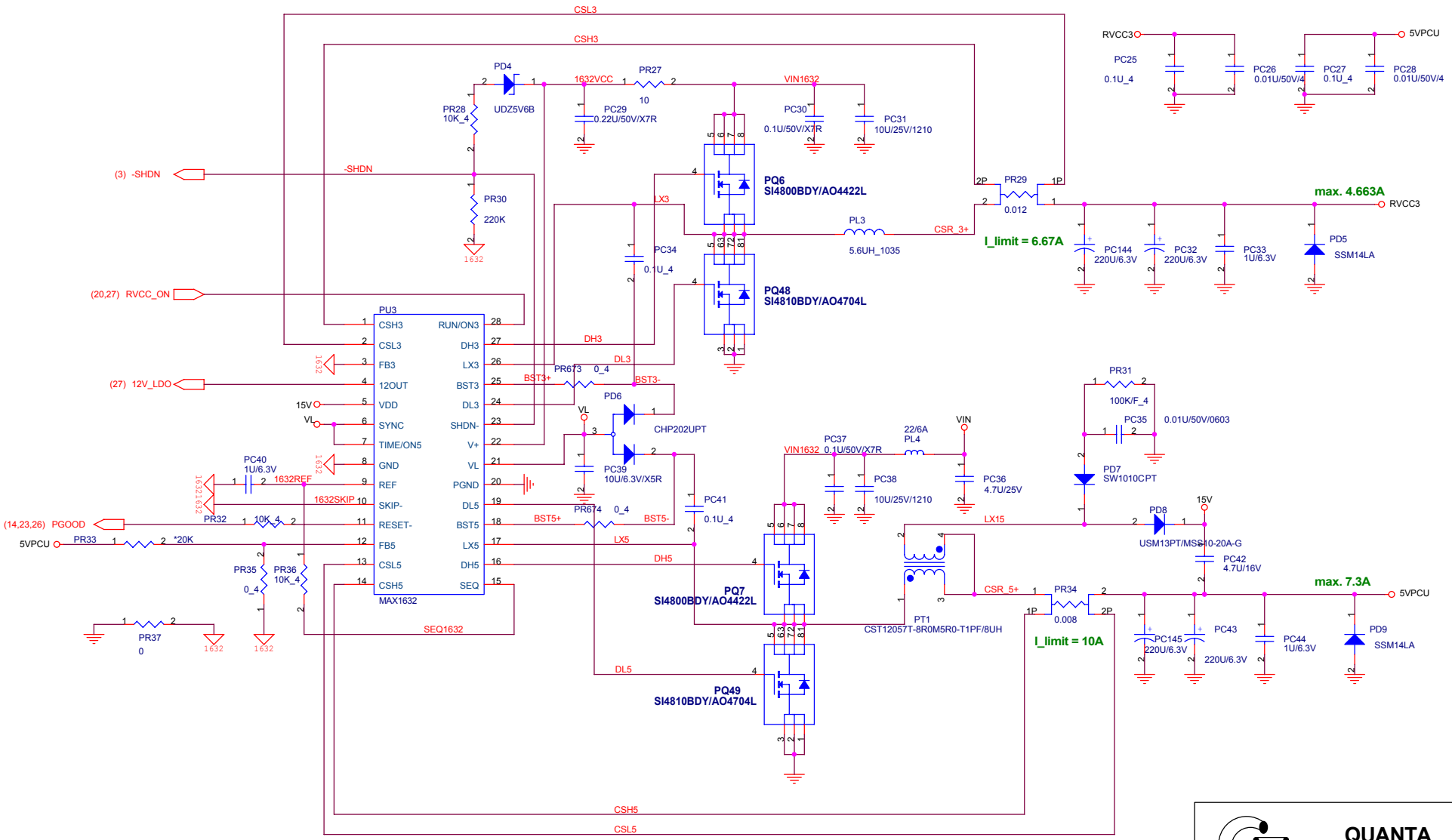

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QUANTA COMPUTER

Size	Document Number	Rev
	RD1 Main Board	1A
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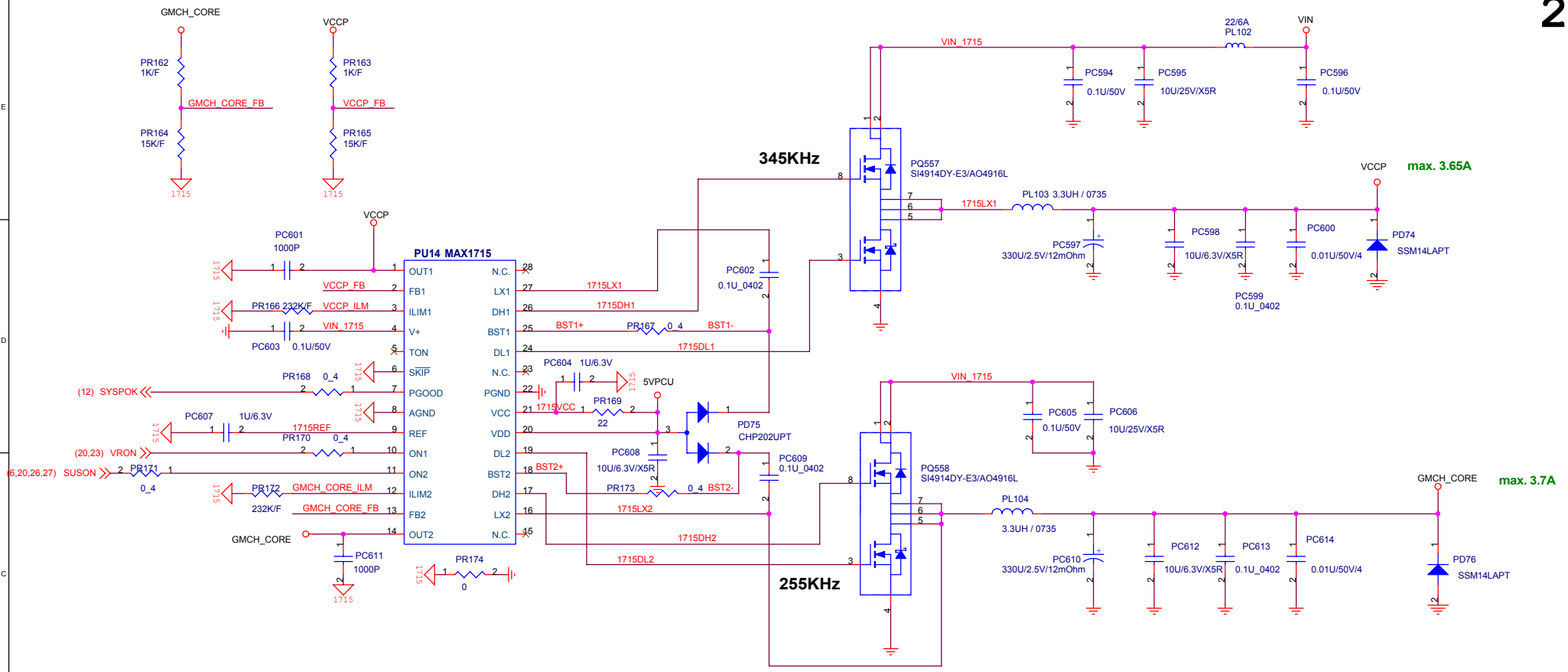

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QUANTA COMPUTER

Size	Document Number	Rev
	RD1 Main Board	1A
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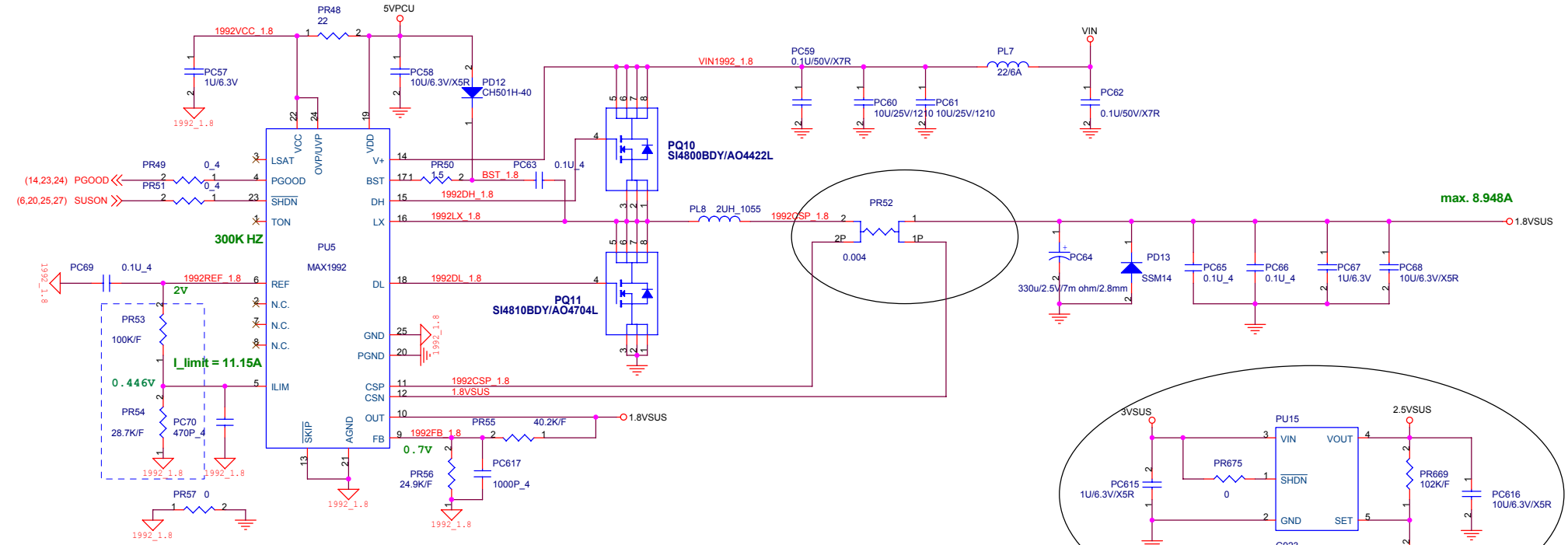

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QUANTA COMPUTER

Size	Document Number	Rev
	RD1 Main Board	1A
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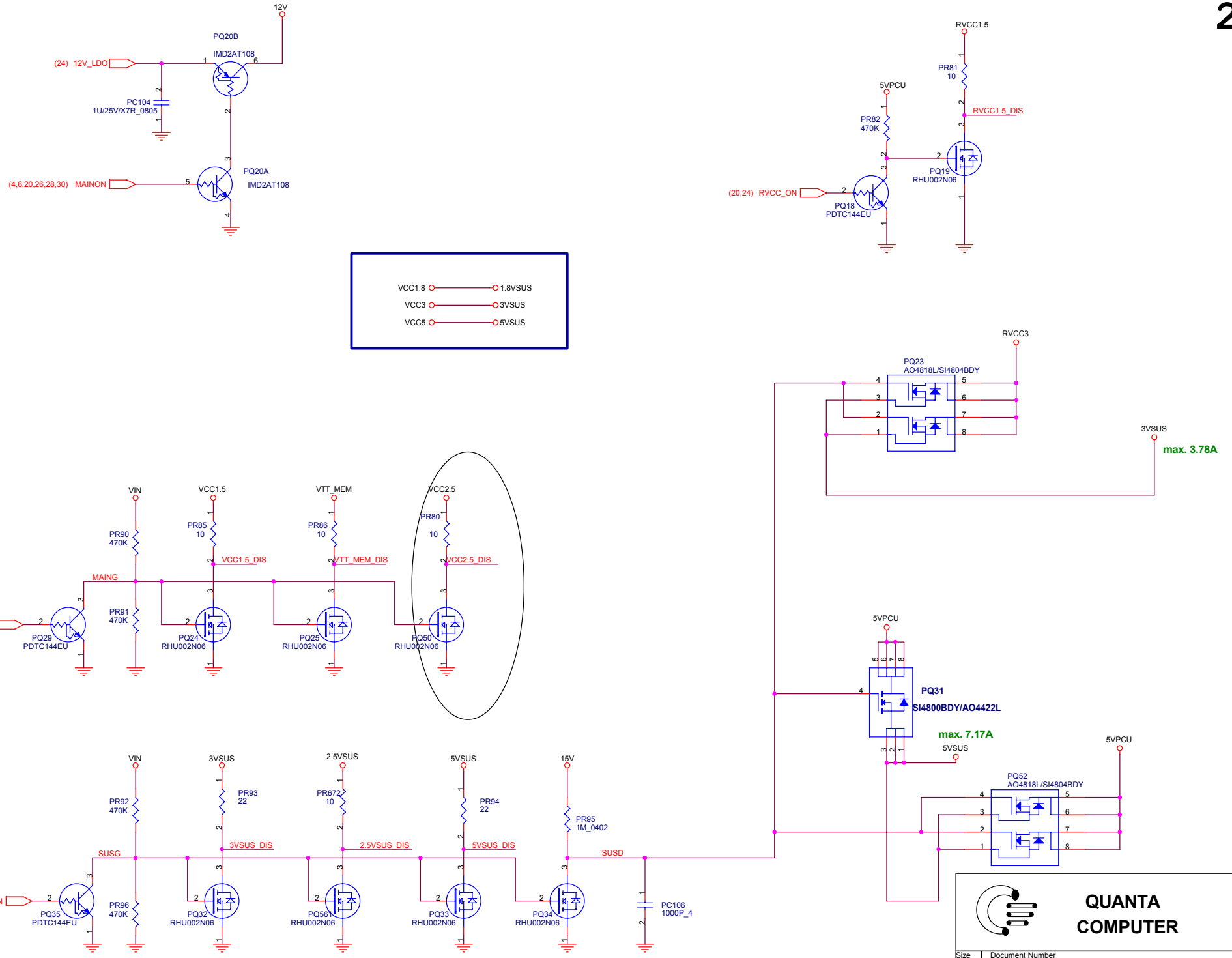

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QUANTA COMPUTER

Size	Document Number	Rev
	RD1 Main Board	1A
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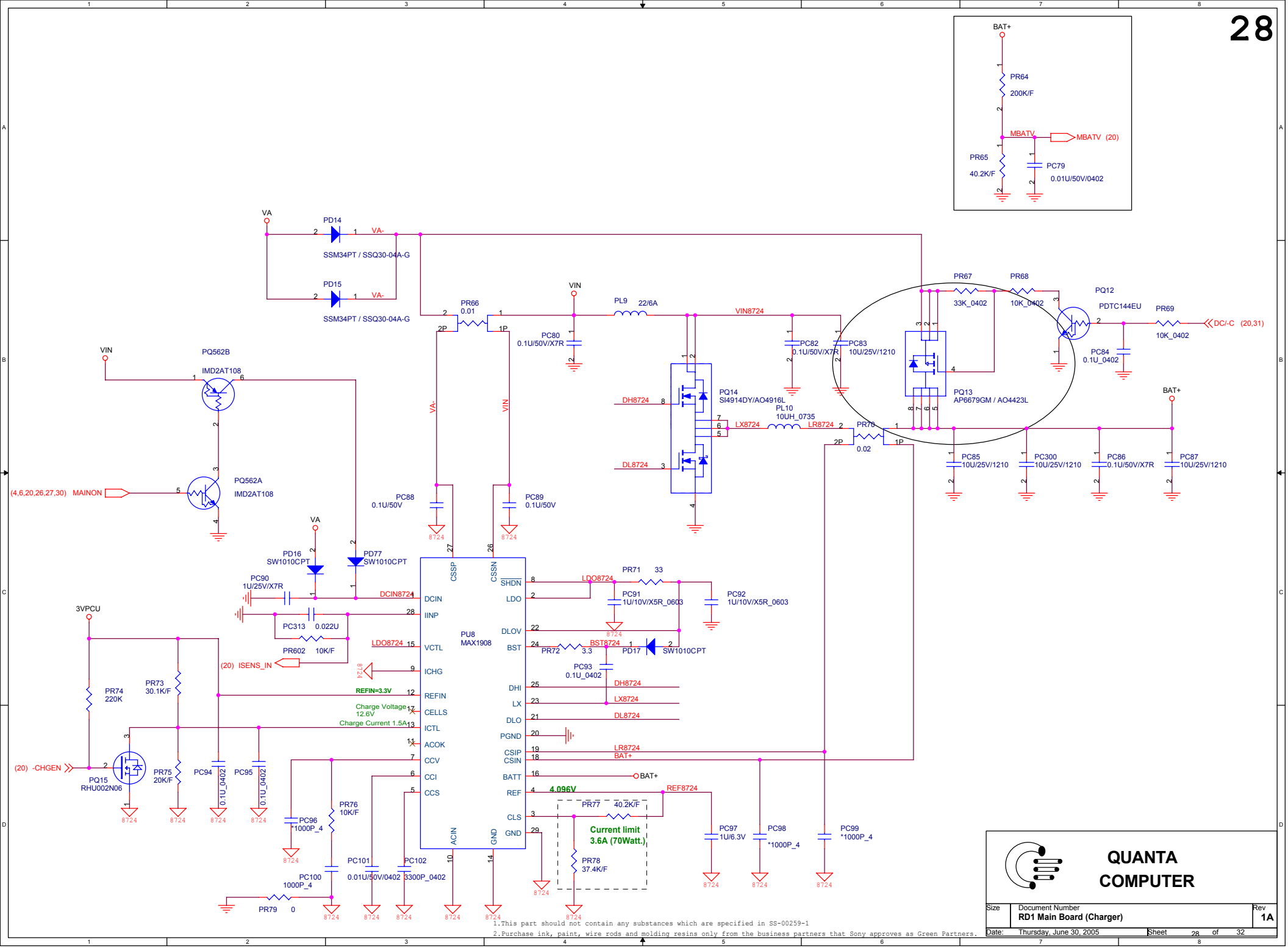
BAT+
PR64
200K/F
MBATV
MBATV (20)
PR65
40.2K/F
PC79
0.01U/50V/0402
VA
PD14
SSM34PT / SSQ30-04A-G
VA-
PD15
SSM34PT / SSQ30-04A-G
PR66
0.01
VIN
PL9
22/6A
VIN8724
PC80
0.1U/50V/X7R
PR67
33K_0402
PR68
10K_0402
PQ12
PDTC144EU
PR69
10K_0402
DC/-C (20,31)
PC84
0.1U_0402
PC82
0.1U/50V/X7R
PC83
10U/25V/1210
PQ13
AP6679GM / AO4423L
PQ562B
IMD2AT108
PQ562A
IMD2AT108
(4,6,20,26,27,30) MAINON
PQ14
SI4914DY/AO4916L
PL10
10UH_0735
DH8724
DL8724
LX8724
LR8724
PR70
0.02
PC85
10U/25V/1210
PC300
10U/25V/1210
PC86
0.1U/50V/X7R
PC87
10U/25V/1210
PC88
0.1U/50V
PC89
0.1U/50V
PD16
SW1010CPT
PD77
SW1010CPT
PC90
1U/25V/X7R
DCIN8724
PC313
0.022U
PR602
10K/F
(20) ISENS_IN
3VPCU
PR74
220K
PR73
30.1K/F
REFIN=3.3V
Charge Voltage
12.6V
Charge Current 1.5A
(20) -CHGEN
PQ15
RHU002N06
PR75
20K/F
PC94
0.1U_0402
PC95
0.1U_0402
PR76
10K/F
PC96
*1000P_4
PC100
1000P_4
PR79
0
PC101
0.01U/50V/0402
PC102
3300P_0402
PU8
MAX1908
CSSP
CSSN
SHDN
LDO
DLOV
BST
DHI
LX
DLO
PGND
CSIP
CSIN
BATT
REF
CLS
GND
DCIN
IINP
VCTL
ICHG
REFIN
CELLS
ICTL
ACOK
CCV
CCI
CCS
ACIN
LDO8724
PR71
33
PC91
1U/10V/X5R_0603
PC92
1U/10V/X5R_0603
PR72
3.3
BST8724
PD17
SW1010CPT
PC93
0.1U_0402
4.096V
REF8724
PR77
40.2K/F
Current limit
3.6A (70Watt.)
PR78
37.4K/F
PC97
1U/6.3V
PC98
*1000P_4
PC99
*1000P_4
1.This part should not contain any substances which are specified in SS-00259-1
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QUANTA
COMPUTER
Size
Document Number
RD1 Main Board (Charger)
Rev
1A
Date: Thursday, June 30, 2005
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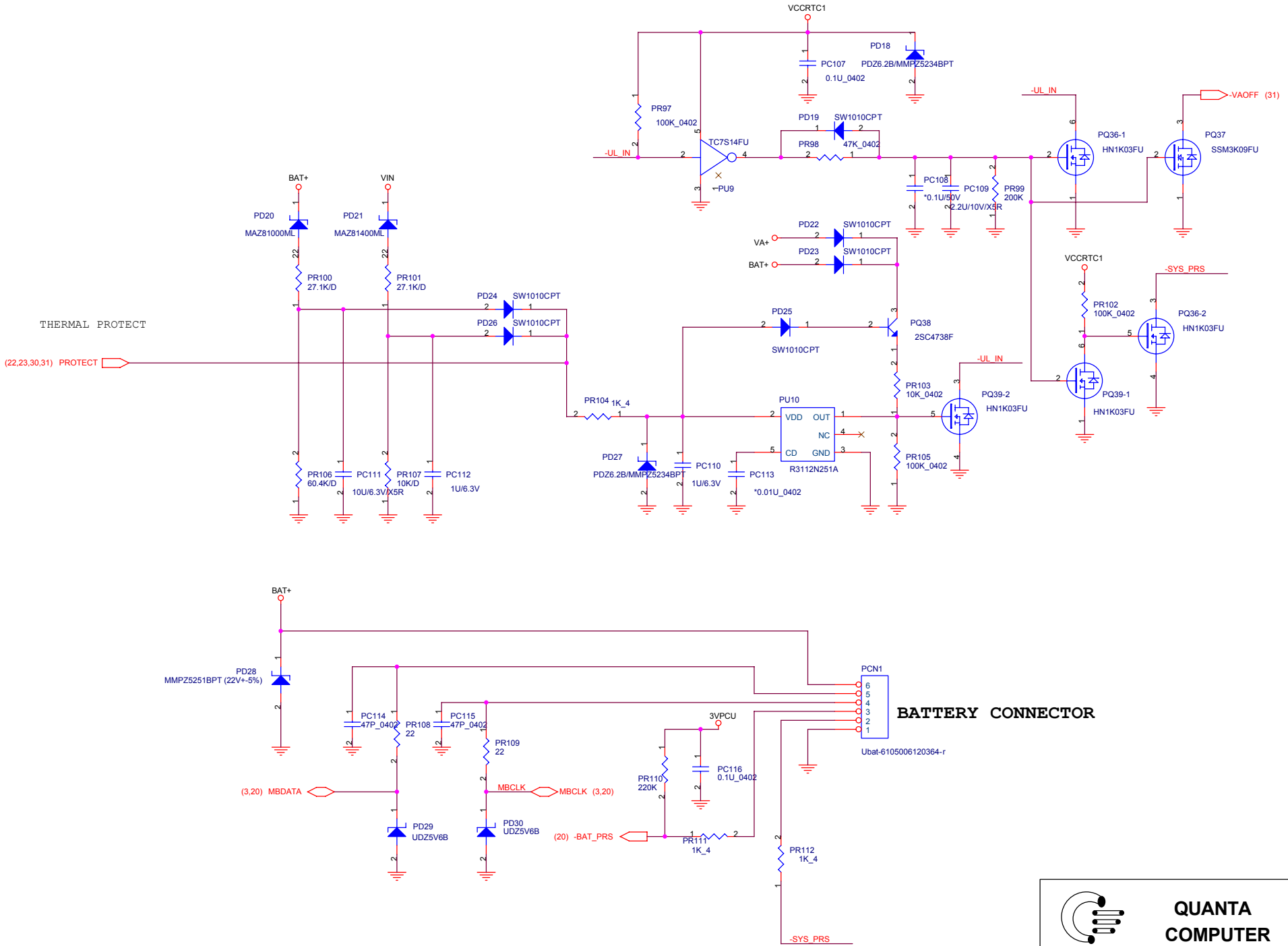

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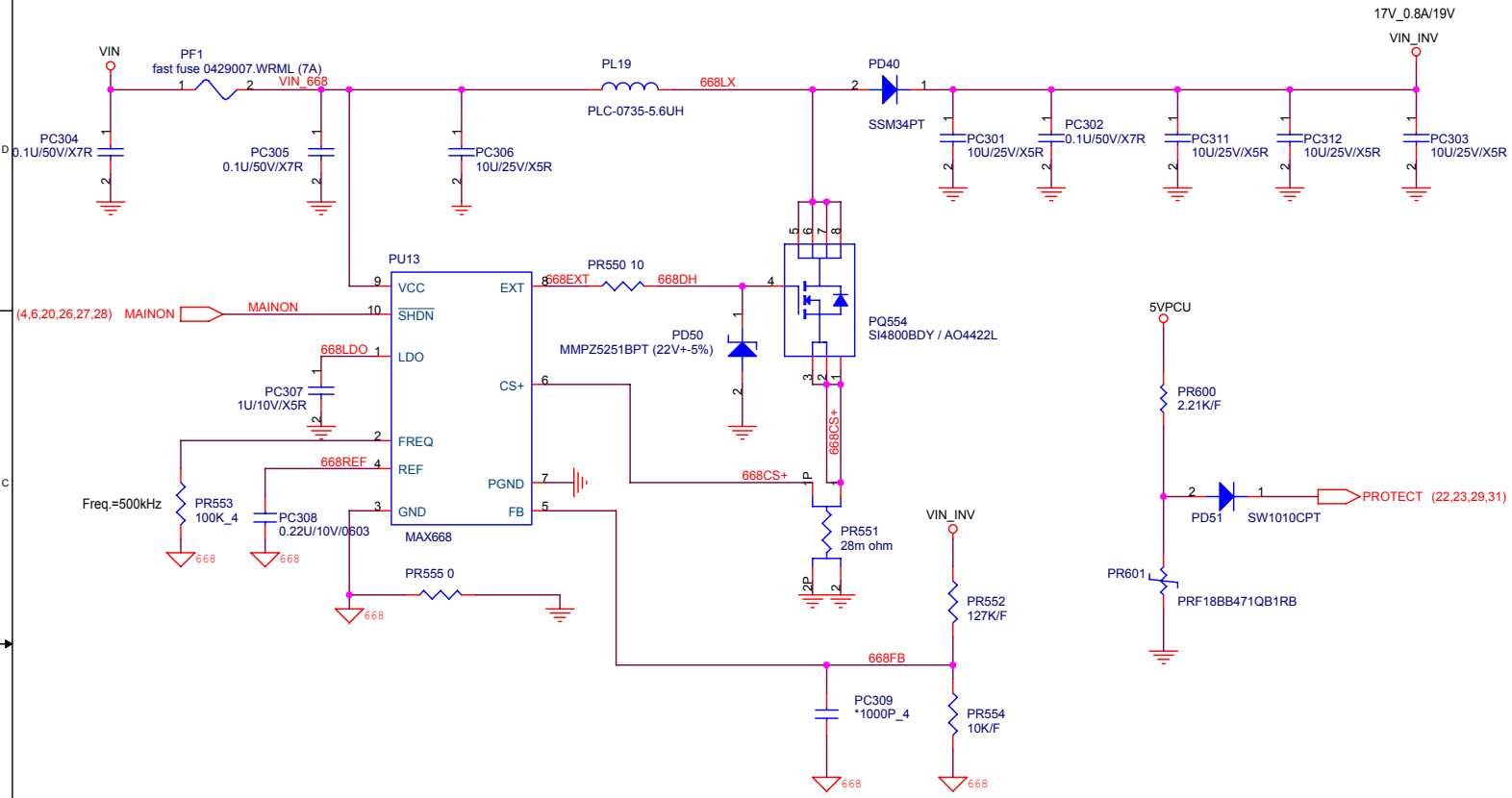
QUANTA COMPUTER

Size	Document Number	Rev
	RD1 Main Board (Charger)	1A
Date: Thursday, June 30, 2005	Sheet 29 of 32	



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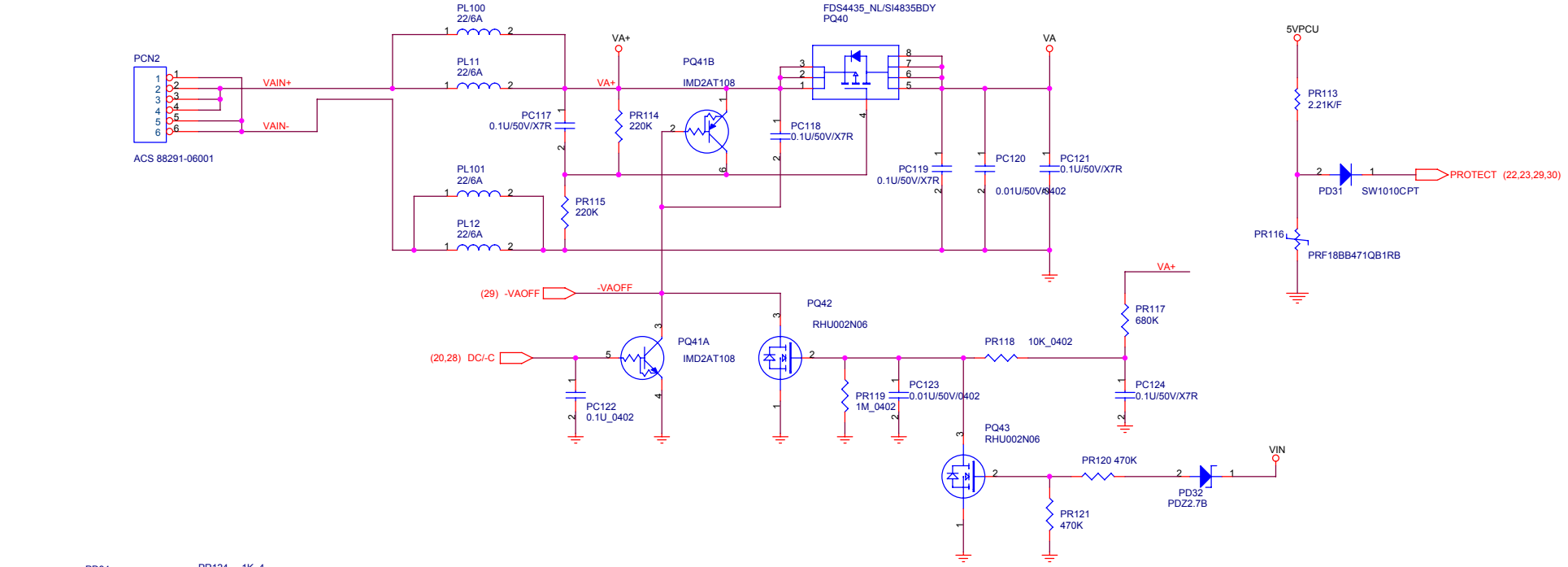
QUANTA COMPUTER

Size	Document Number	Rev
	RD1 Main Board	1A
Date: Thursday, June 30, 2005	Sheet 30 of 32	



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QUANTA COMPUTER

Size	Document Number	Rev
	RD1 Main Board	1A
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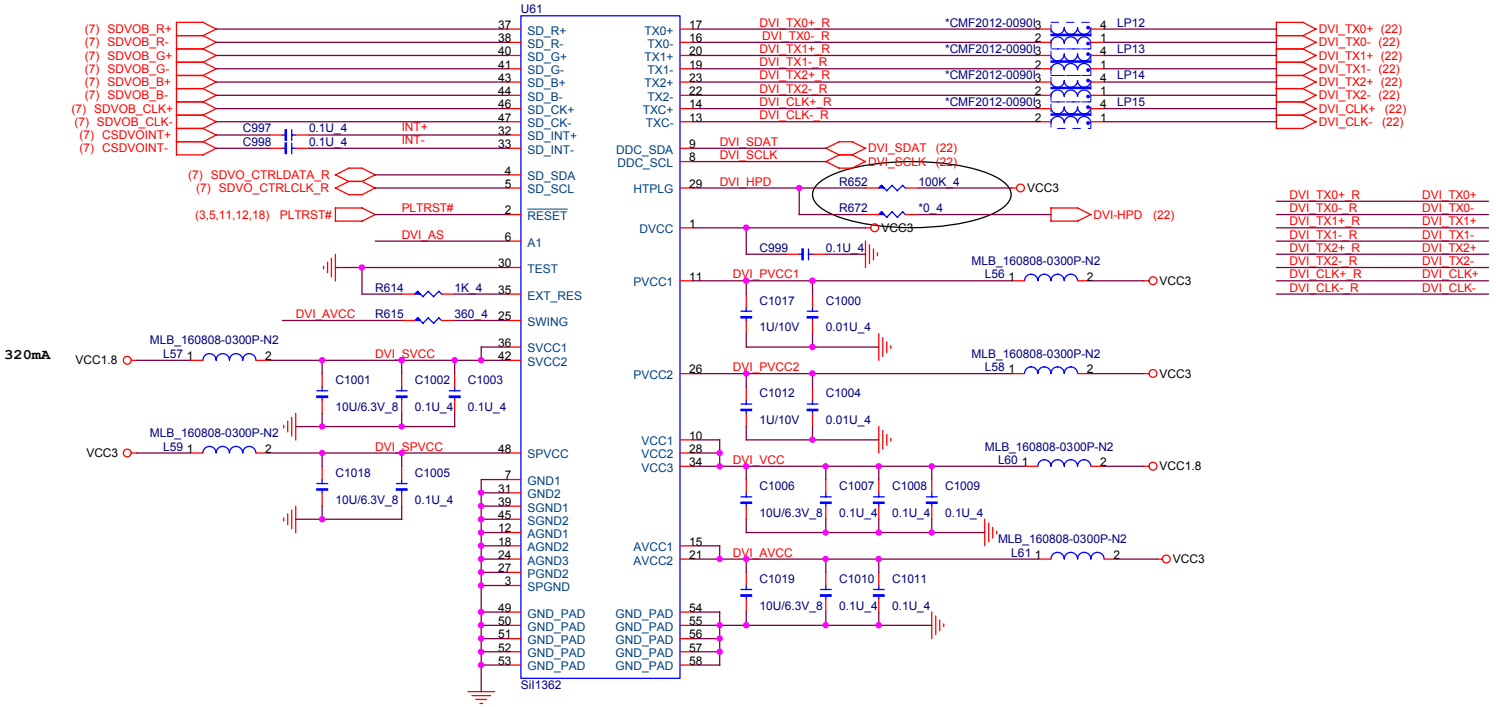

QUANTA COMPUTER

Title	DVI	
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